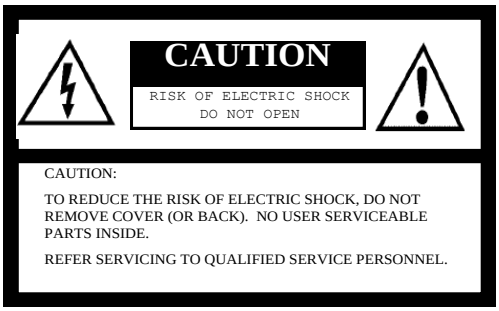
SENTECH

CMOS Camera Link Series

Small Cubic Type, 2MP / 4MP CMOS Color / Monochrome Camera Link Camera

Safety Precautions



The lightning flash with arrowhead symbol, within an equilateral triangle, is intended to alert the user to the presence of uninsulated "dangerous voltage" within the product's enclosure that may be of sufficient magnitude to constitute a risk of electric shock to persons.

The exclamation point within an equilateral triangle is intended to alert the user to the presence of important operating and maintenance (servicing) instructions in the literature accompanying the appliance.

For U.S.A.

Warning:

This equipment generates and uses radio frequency energy and if not installed and used properly, I.e., in strict accordance with the instruction manual, may cause harmful interference to radio communications. It has been tested and found to comply with the limits for a Class A computing device pursuant to Subpart J of Part 15 of FCC Rules, which are designed to provide reasonable protection against such interference when operated in a commercial environment.

For Canada

Warning:

This digital apparatus does not exceed the Class A limits for radio noise emissions from digital apparatus set out in the Radio Interference Regulations of the Canadian Department of Communications.

WARNING:

TO PREVENT FIRE OR SHOCK HAZARD, DO NOT EXPOSE THIS APPLIANCE TO RAIN OR MOISTURE.

Product Precautions

- Handle the camera with care. Do not abuse the camera. Avoid striking or shaking it. Improper handling or storage could damage the camera.
- Do not pull or damage the camera cable.
- During camera use, do not wrap he unit in any material. This will cause the internal temperature of the unit to increase.
- Do not expose the camera to moisture, or do not try to operate it in wet areas.
- Do not operate the camera beyond its temperature, humidity and power source ratings.
- While the camera is not being used, keep the lens or lens cap on the camera to prevent dust or contamination from getting in the CCD or filter area and scratching or damaging this area.
- Do not keep the camera under the following conditions:
    - In wet, moist, and high humidity areas
    - Under hot direct sunlight
    - In high temperature areas
    - Near an object that releases a strong magnetic or electric field
    - Areas with strong vibrations
- Use a soft cloth to clean the camera. Use pressured air spray to clean the surface of the glass. DO not scratch the surface of the glass.

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Contents

I. Introduction 8
A. Features 8
B. Naming Specification 8
II. Specifications 9-18
A. Electronic / Mechanical / Environmental Specifications 9-10
1. STC-CMB2MCL / STC-CMC2MCL 9
2. STC-CMB4MCL / STC-CMC4MCL 10
B. Spectral Sensitivity Characteristics 11-12
1. STC-CMB2MCL / STC-CMB4MCL 11
2. STC-CMC2MCL / STC-CMC4MCL 11
3. STC-CMC2MCL-NIR / STC-CMC4MCL-NIR (Near IR Model) 12
C. Connector Specifications 13-16
1. Camera Link Connectors 13
2. Power / IO Connector 14-16
D. Dimensions 17
E. Accuracy of Sensor Position 18
III. Camera Installation 19
IV. Camera Output Timing Charts 20-58
A. Horizontal Timing (STC-CMB2MCL / CMC2MCL / STC-CMB4MCL / CMC4MCL) 20-36
1. 2 TAPS (1X2-1Y) / Horizontal 2,048 Pixels 20
2. 4 TAPS (1X4-1Y) / Horizontal 2,048 Pixels 21
3. 8 TAPS (1X8-1Y) / Horizontal 2,048 Pixels 22-23
4. 10 TAPS (1X10-1Y) / Horizontal 2,040 Pixels 24-25
5. 2 TAPS (1X2-1Y) / Horizontal 1,024 Pixels 26
6. 2 TAPS (1X2-1Y) / Horizontal 512 Pixels 27
7. 4 TAPS / Horizontal 1,024 Pixels 28
8. 2 TAPS (1X2-1Y) / 2 x 2 Binning 29
9. 4 TAPS (1X4-1Y) / 2 x 2 Binning 30

10. 2 TAPS (1X2-1Y) / 4 x 4 Binning 31
11. 1 TAPS (1X-1Y) / 8 x 8 Binning 32
12. 2 TAPS (1X2-1Y) / 2 x 2 Subsampling 33
13. 4 TAPS (1X4-1Y) / 2 x 2 Subsampling 34
14. 2 TAPS (1X2-1Y) / 4 x 4 Subsampling 35
15. 1 TAPS (1X-1Y) / 8 x 8 Subsampling 36
B. Vertical Timing 37-47
1. Full Scan (STC-CMB2MCL / CMC2MCL) 39
2. Full Scan (STC-CMB4MCL / CMC4MCL) 40
3. 2 x 2 Binning (STC-CMB2MCL / CMC2MCL) 41
4. 4 x 4 Binning (STC-CMB2MCL / CMC2MCL) 41
5. 8 x 8 Binning (STC-CMB2MCL / CMC2MCL) 41
6. 2 x 2 Subsampling (STC-CMB2MCL / CMC2MCL) 43
7. 4 x 4 Subsampling (STC-CMB2MCL / CMC2MCL) 43
8. 8 x 8 Subsampling (STC-CMB2MCL / CMC2MCL) 43
9. 2 x 2 Binning (STC-CMB4MCL / CMC4MCL) 45
10. 4 x 4 Binning (STC-CMB4MCL / CMC4MCL) 45
11. 8 x 8 Binning (STC-CMB4MCL / CMC4MCL) 45
12. 2 x 2 Subsampling (STC-CMB4MCL / CMC4MCL) 47
13. 4 x 4 Subsampling (STC-CMB4MCL / CMC4MCL) 47
14. 8 x 8 Subsampling (STC-CMB4MCL / CMC4MCL) 47
C. AOI Timing 49-51
D. Camera Link Bit Assignment 52-55
E. Camera Link Tap Geometry 56-57
1. 2 Tap (1X2-1Y) 56
2. 4 Tap (1X4-1Y) 56
3. 8 Tap (1X8-1Y) 57
4. 10 Tap (1X10-1Y) 57
F. Bayer Pattern for Color Models 58
V. Camera Function Modes 59-63
A. Normal Mode 59
1. Normal Mode (Electronic Shutter) 59

B. Pulse Width Trigger Mode 60-61
1. Pulse Width Trigger Mode (V-Reset) 60
2. Pulse Width Trigger Mode (Exposure Timing) 61
C. Edge Preset Trigger Mode 62-63
1. Edge Preset Trigger Mode (V-Reset) 62
2. Edge Preset Trigger Mode (Exposure Timing) 63
VI. The Communication Protocol Specifications 64-78
A. The Communication Method 64
B. The Communication Settings 64
C. The Communication Format 64-65
D. The Camera Control Commands 66-78
1. The Camera Commands List 66-67
2. Descriptions of the Camera Control Commands 68-77
3. The Sequence for Saving Commands to the EEPROM 78
VII. Control Software 79-86
A. Summary 79-81
1. File 80
2. Comm 80
3. Mode 81
4. Help 81
B. Software Function (Standard) 81-84
1. Shutter 81
2. Mode 82
3. Gain 83
4. Serial Communication 83
5. Flip 83
6.Other 84
C. Software Function (Partial) 85
D. Software Function (Advanced) 85
E. Software Function (HDR) 85
F. Software Function (SP_Pin) 86
VIII. Actual Camera Settings & Technical Notes 87-89

A. Using the Trigger Signal Through the 6pin 87

B. Example setting of AOI 88-89

I. Introduction

This document describes the specifications and users guide of cameras described below.

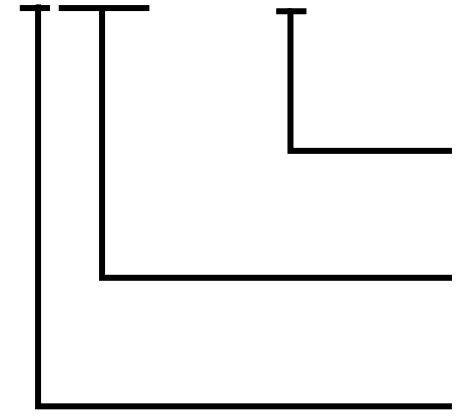
STC-CMB2MCL / STC-CMB2MCL-NIR	(2M Monochrome / Near IR)
STC-CMC2MCL	(2M Color)
STC-CMB4MCl / STC-CMB4MCL-NIR	(4M Monochrome / Near IR)
STC-CMC4MCL	(4M Color)

A. Features

- CMOS Sensor
- Camera Link (Full, Medium, Base Configuration)
- 10,8,4,2 TAP
- 2 x 2, 4 x 4, 8 x 8 Binning and 2 x 2, 4 x 4, 8 x 8 Subsampling
- PoCL
- Support Near IR Sensor

B. Naming Specification

STC-CMxxMCL-x



None: Standard
NIR: Near IR

Resolution
2M: 2MPl
4M: 4MP

C: Color
B: Monochrome

II. Specifications

A. Electronic Specifications / Mechanical Specifications / Environmental Specifications

1. STC-CMB2MCL (2MP, Monochrome) / STC-CMB2MCL-NIR (2MP, Near IR) / STC-CMC2MCL (2MP, Color)

Product			STC-CMC2MCL		STC-CMB2MCL
Electronic specifications	Imager		2/3" Meg color progressive CMOS (CMOSIS: CMV2000)		2/3" Meg monochrome progressive CMOS (CMOSIS: CMV2000)
	Active picture elements		2048 (H) x 1088 (V)		
	Chip size		11.264×5.984 mm		
	Cell size		5.5 (H) x 5.5 (V) µm		
	Scanning system		Progressive		
	Scanning method		Full scanning, Variable AOI		Full scanning, Variable AOI Binning scanning, Binning variable AOI
	Pixel frequency of the sensor	1X2-1Y 1X4-1Y 1X8-1Y 1X10-1Y	(8bit/10bit): (8bit/10bit): (8bit): (8bit):	10.625MHz (2,048 x 1,088), 21.250MHz (1,024 x 1,088), 42.500MHz (512 x 1,088) 21.250MHz (2,048 x 1,088), 42.500MHz (1,024 x 1,088) 42.500MHz (2,048 x 1,088) 48.000MHz (2,040 x 1,088)	
	Frame rate Vertical frequency of the Camera Link output	1X2-1Y 1X4-1Y 1X8-1Y 1X10-1Y	(8bit/10bit): (8bit/10bit): (8bit): (8bit):	73.8fps (2,048 x 1,088),147.6fps (1,024 x 1,088),295.2fps (512 x 1,088) 147.6fps (2,048 x 1,088), 295.2fps (1,024 x 1,088) 295.2fps (2,048 x 1,088) 333.4fps (2,040 x 1,088)	
	Horizontal frequency of the Camera Link output	1X2-1Y 1X4-1Y 1X8-1Y 1X10-1Y	(8bit/10bit): (8bit/10bit): (8bit): (8bit):	82kHz (2,048 x 1,088), 164kHz (1,024 x 1,088), 329kHz (512 x 1,088) 164kHz (2,048 x 1,088), 329kHz (1,024 x 1,088) 329kHz (2,048 x 1,088) 372kHz (2,040 x 1,088)	
	Pixel frequency o the Camera Link output	1X2-1Y 1X4-1Y 1X8-1Y 1X10-1Y	(8bit/10bit): (8bit/10bit): (8bit): (8bit):	85MHz/42.5MHz 85MHz/42.5MHz 85MHz/42.5MHz 80MHz/40MHz	
	Noise level (8bit output)		Less than 3 Digit (Gain 0 dB)		
	Dynamicrange		60 dB		
	Minimum scene illumination *Near IR model (-NIR)		6 Lux at F1.2		2 Lux at F1.2
			-		TBD Lux at F1.2
	Sync. System		Internal		
	Video output	'@8bit output	10TAP / FULL / MIDEUM / BASE configuration		
		'@10bit output	MIDEUM / BASE configuration		
	Shutter speed		45 seconds to 25.8u seconds (Variable at line)		
	Digital gain		1x to 4x		
	Gamma		1.0		
	Power	Input voltage	12Vdc ± 10% (PoCL or Power/IO connector)		
		Consumption	Less than 4.5 W		
	Operation mode		Free-run, Edge preset trigger (V-reset), Pulse width trigger (V-reset)		
	Communication		RS232 via Camera Link connector		
Mechanical specifications	Dimensions		50 (W) x 50 (H) x 40.5 (D) mm (Excluding the connector)		
	Optical filter		No IR cut filter		
	Material		Aluminum (AC)		
	Lens mount		C mount		
	Interface connector		Camera Link connector: SDR connector x 2 Power/IO connector: HR10A-7R-6PB (Hirose) or equivalent		
	Weight		Approximately 140 g		
Environmental specifications	Operational temperature		'-5 to 40 deg. C		
	Storage temperature		'-30 to 65 deg. C		
	Vibration		20Hz to 200Hz to 20Hz (5min./cycle), acceleration 10G, XYZ 3 directions 30 min. each)		
	Shock		Acceleration 38G, half amplitude 6ms, XYZ 3 directions 3times each		
	Standard compliancy		EMS: EN61000-6-2, EMI: EN55022 (Class B)		
	RoHS		RoHS compliance		

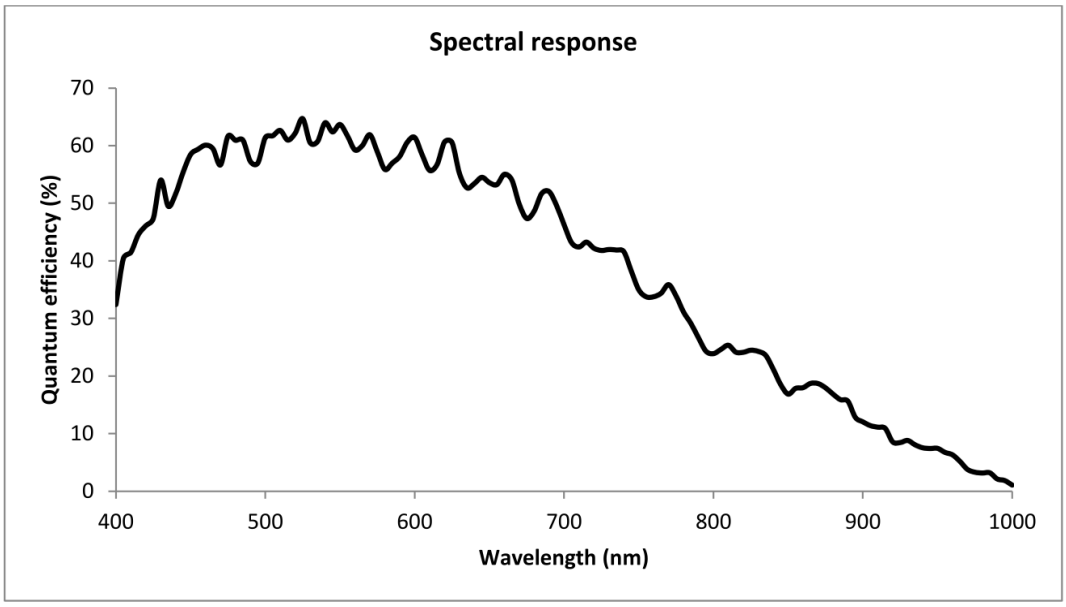
2. STC-CMB4MCL (4MP, Monochrome) / STC-CMB4MCL-NIR (4MP, Near IR) / STC-CMC4MCL (4MP, Color)

Product			STC-CMC4MCL		STC-CMB4MCL
Electronic specifications	Imager		1" 4Meg color progressive CMOS (CMOSIS: CMV4000)		1" 4Meg monochrome progressive CMOS (CMOSIS: CMV4000)
	Active picture elements		2048 (H) x 2048 (V)		
	Chip size		11.264×11.264 mm		
	Cell size		5.5 (H) x 5.5 (V) µm		
	Scanning system		Progressive		
	Scanning method		Full scanning, Variable AOI		Full scanning, Variable AOI Binning scanning, Binning variable AOI
	Pixel frequency of the sensor	1X2-1Y	(8bit/10bit):	10.625MHz (2,048 x 2,048), 21.250MHz (1,024 x 2,048), 42.500MHz (512 x 2,048)	
		1X4-1Y	(8bit/10bit):	21.250MHz (2,048 x 2,048), 42.500MHz (1,024 x 2,048)	
		1X8-1Y	(8bit):	42.500MHz (2,048 x 2,048)	
		1X10-1Y	(8bit):	48.000MHz (2,040 x 2,048)	
	Frame rate Vertical frequency of the Camera Link output	1X2-1Y	(8bit/10bit):	39.7fps (2,048 x 2,048), 79.3fps (1,024 x 2,048), 158.7fps (512 x 2,048)	
		1X4-1Y	(8bit/10bit):	79.3fps (2,048 x 2,048), 158.7fps (1,024 x 2,048)	
		1X8-1Y	(8bit):	158.7fps (2,048 x 2,048)	
		1X10-1Y	(8bit):	179.2fps (2,040 x 2,048)	
	Horizontal frequency of the Camera Link output	1X2-1Y	(8bit/10bit):	82kHz (2,048 x 2,048), 164kHz (2,024 x 1,048), 329kHz (512 x 2,048)	
		1X4-1Y	(8bit/10bit):	164kHz (2,048 x 2,048), 329kHz (1,024 x 2,048)	
		1X8-1Y	(8bit):	329kHz (2,048 x 2,048)	
		1X10-1Y	(8bit):	372kHz (2,040 x 2,048)	
	Pixel frequency o the Camera Link output	1X2-1Y	(8bit/10bit):	85MHz/42.5MHz	
		1X4-1Y	(8bit/10bit):	85MHz/42.5MHz	
		1X8-1Y	(8bit):	85MHz/42.5MHz	
		1X10-1Y	(8bit):	80MHz/40MHz	
	Noise level (8bit output)		Less than 3 Digit (Gain 0 dB)		
	Dynamicrange		60 dB		
	Minimum scene illumination		6 Lux at F1.2		2 Lux at F1.2
	*Near IR		-		TBD Lux at F1.2
	Sync. System		Internal		
	Video output	'@8bit output	10TAP / FULL / MIDEUM / BASE configuration		
		'@10bit output	MIDEUM / BASE configuration		
	Shutter speed		45 seconds to 25.8u seconds (Variable at line)		
	Digital gain		1x to 4x		
	Gamma		1.0		
	Power	Input voltage	12Vdc ± 10% (PoCL or Power/IO connector)		
		Consumption	Less than 4.5 W		
	Operation mode		Free-run, Edge preset trigger (V-reset), Pulse width trigger (V-reset)		
	Communication		RS232 via Camera Link connector		
Mechanical specifications	Dimensions		50 (W) x 50 (H) x 40.5 (D) mm (Excluding the connector)		
	Optical filter		No IR cut filter		
	Material		Aluminum (AC)		
	Lens mount		C mount		
	Interface connector		Camera Link connector: SDR connector x 2 Power/IO connector: HR10A-7R-6PB (Hirose) or equivalent		
	Weight		Approximately 140 g		
Environmental specifications	Operational temperature		'-5 to 40 deg. C		
	Storage temperature		'-30 to 65 deg. C		
	Vibration		20Hz to 200Hz to 20Hz (5min./cycle), acceleration 10G, XYZ 3 directions 30 min. each)		
	Shock		Acceleration 38G, half amplitude 6ms, XYZ 3 directions 3times each		
	Standard compliancy		EMS: EN61000-6-2, EMI: EN55022 (Class B)		

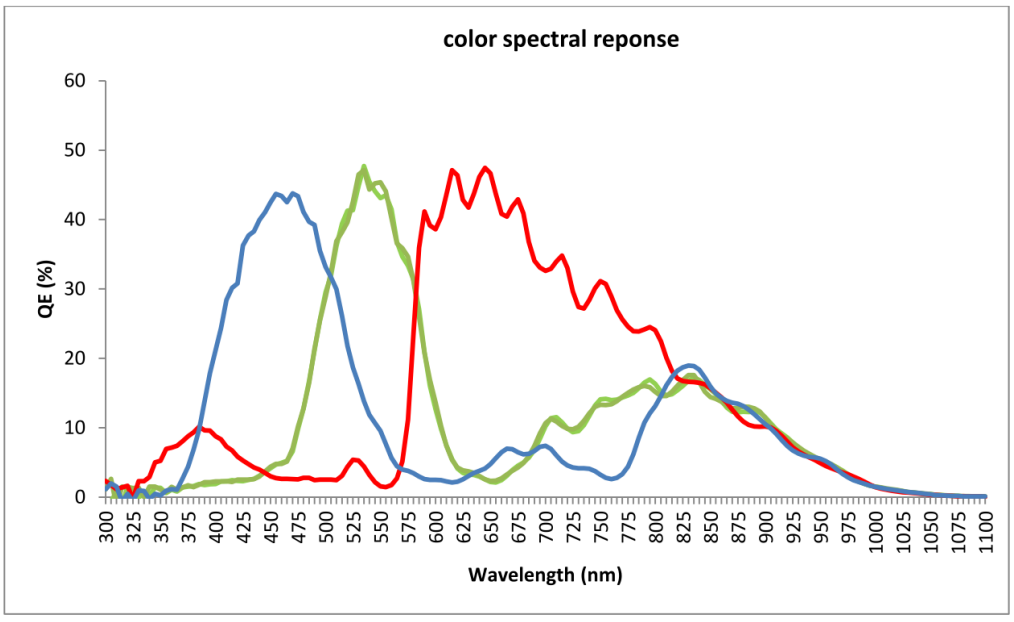
	RoHS	RoHS compliance

B. Spectral Sensitivity Characteristics

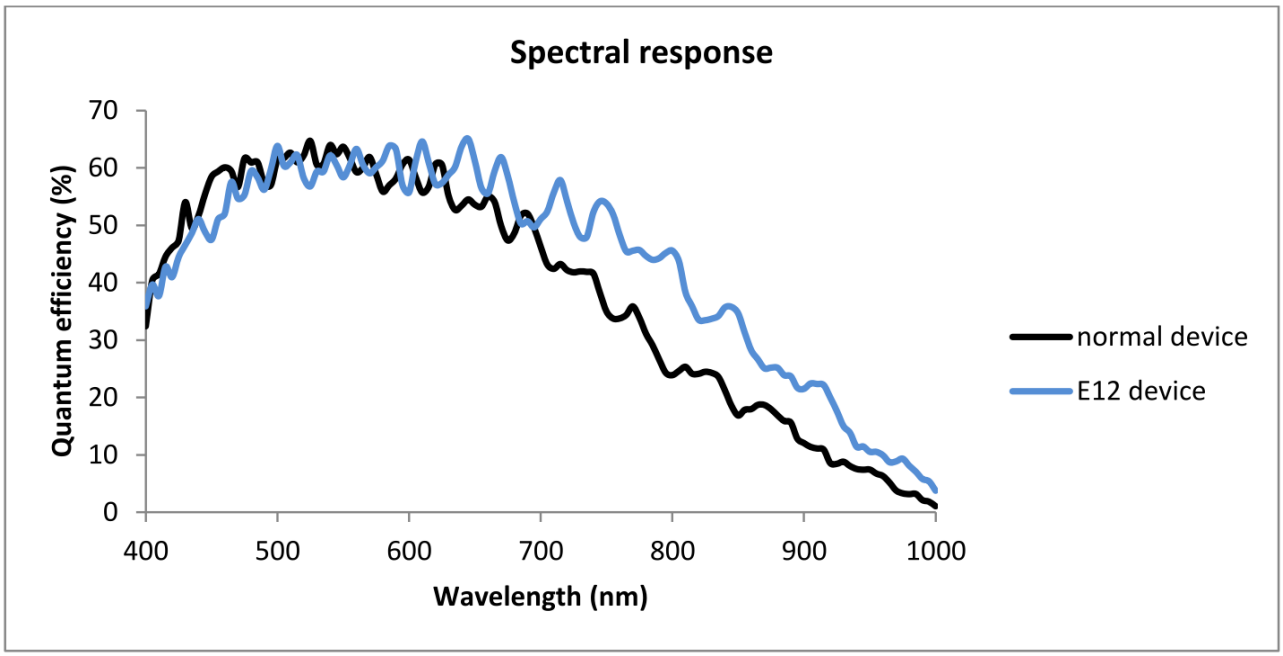
1. STC-CMB2MCL / STC-CMB4MCL



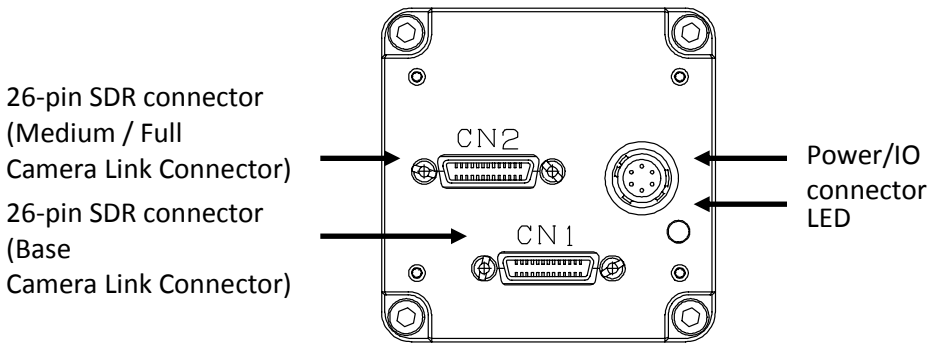
2. STC-CMC2MCL / STC-CMC4MCL



3. STC-CMB2MCL-NIR / STC-CMB4MCL-NIR (Near IR Model)



C. Connector Specifications



1. Camera Link Connectors: SDR (3M) Equivalent x 2

(Caution)
This product is a PoCL type.
When the frame grabber board and the cable are compatible with PoCL the frame grabber board supplies power to the camera. In this case, please DO NOT supple the power from the Power/IO connector.

When the frame grabber board and the cable are NOT compatible with PoCL, please input the power from the Power/IO connector.

a. Pin Assignment

Base Camera Link Connector

Pin No.	Signal name	Pin No.	Signal name
1	+12V	14	GND
2	X0-	15	X0+
3	X1-	16	X1+
4	X2-	17	X2+
5	Xclk-	18	Xclk+
6	X3-	19	X3+
7	SerTC+	20	SerTC-
8	SerTFG-	21	SerTFG+
9	CC1- (TRG)	22	CC1+ (TRG)
10	CC2+	23	CC2-
11	CC3-	24	CC3+
12	CC4+	25	CC4-
13	GND	26	+12V

Medium / Full Camera Link Connector

Pin No.	Signal name	Pin No.	Signal name
1	+12V	14	GND
2	Y0-	15	Y0+
3	Y1-	16	Y1+
4	Y2-	17	Y2+
5	Yclk-	18	Yclk+
6	Y3-	19	Y3+
7	100 Ohm	20	100 Ohm
8	Z0-	21	Z0+
9	Z1-	22	Z1+
10	Z2-	23	Z2+
11	Zclk-	24	Zclk+
12	Z3-	25	Z3+
13	GND	26	+12V

2. Power / IO Connector

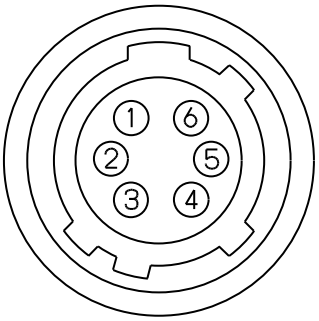
HR10A-7R-6PB (Hirose) or equivalent.

This connector is for 12Vdc power input and the input and output signals.

The trigger input and sync input / output signals can be assigned through the camera setting communication.

a. Pin Assignment

Pin No	Signal Name	IN/OUT	Voltage		
				Low Voltage	High Voltage
1	GND	IN		0V	
2	SP-4	IN/OUT	IN	0 ～ +0.99V	+2.3 ～ +5.0V
			OUT	0V	+3.3V
3	SP-3	IN/OUT	IN	0 ～ +0.99V	+2.3 ～ +5.0V
			OUT	0V	+3.3V
4	SP-2	IN/OUT	IN	0 ～ +0.99V	+2.3 ～ +5.0V
			OUT	0V	+3.3V
5	SP-1	IN/OUT	IN	0 ～ +0.99V	+2.3 ～ +5.0V
			OUT	0V	+3.3V
6	+12Vdc	IN		+12Vdc	

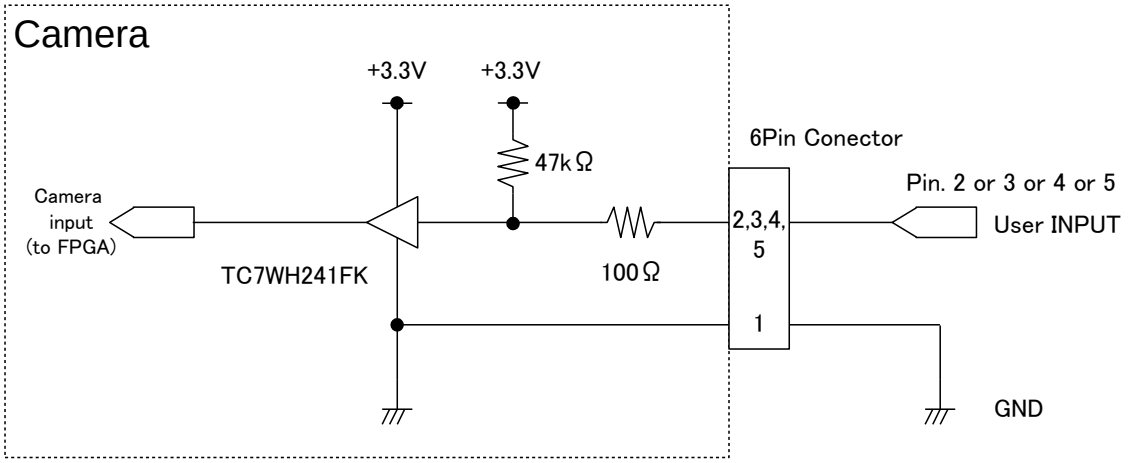


(Note 1)

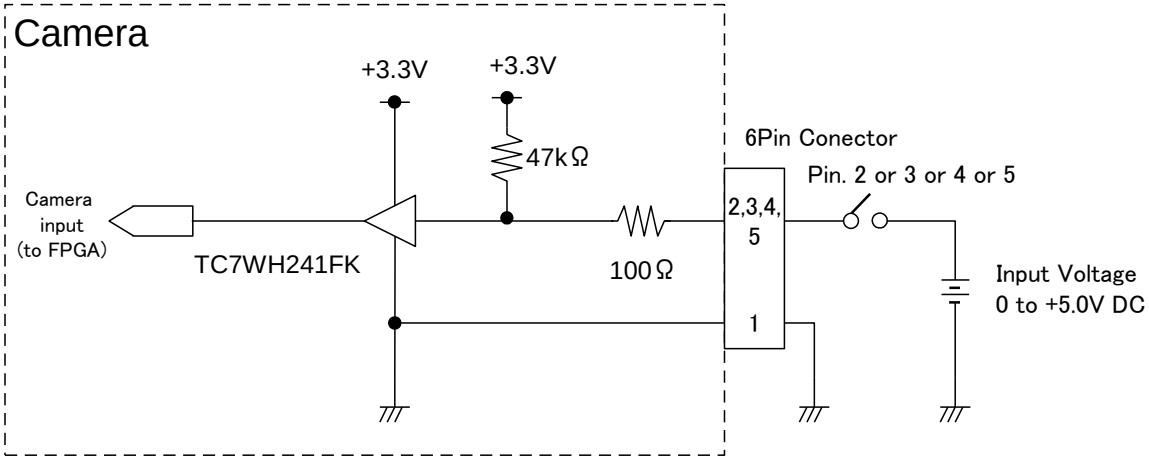
Trigger input signal can be assigned either on Camera Link connector (CC1) or on the No. 2 pin of the power/IO connector through the camera setting communication.

As for the actual setting of the hardware trigger, please refer to Section: VIII-A: Using the Trigger Signal through 6pin

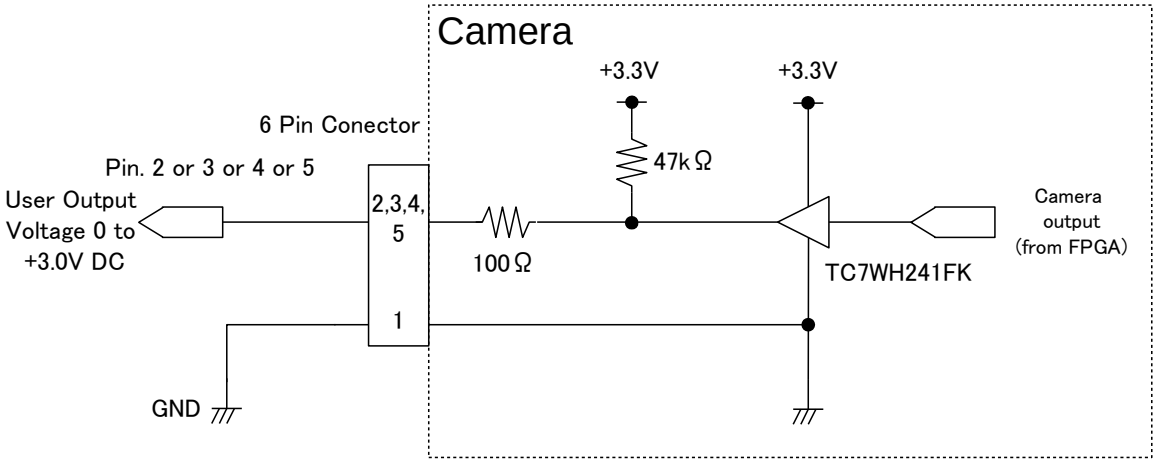
b. Input Signal Circuit



c. Input Signal Circuit Examples

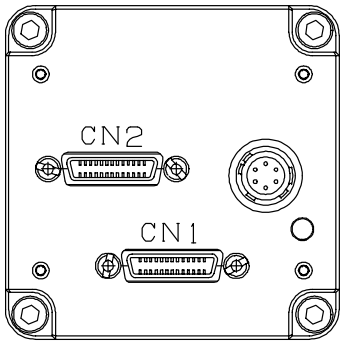


d. Output Signal Circuit / Examples

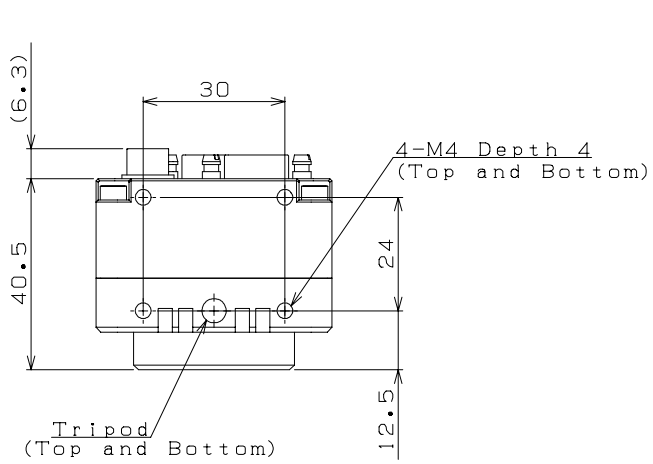


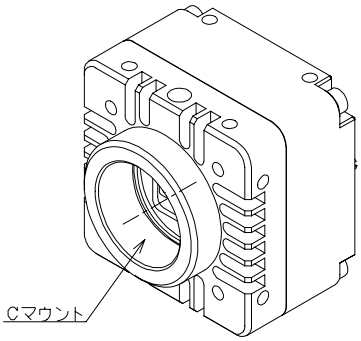
e. LED Information

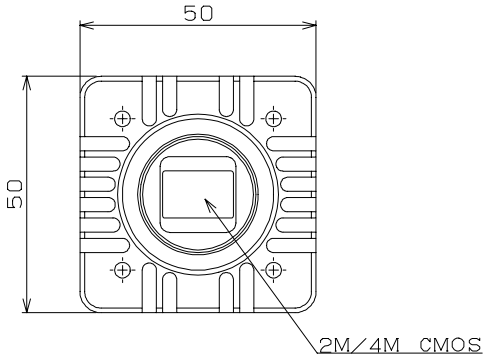
Mode setting	LED
D9H.0 = 1	OFF
Trigger mode	On 1 second then Off 1 second (repeatedly)
Free run	ON

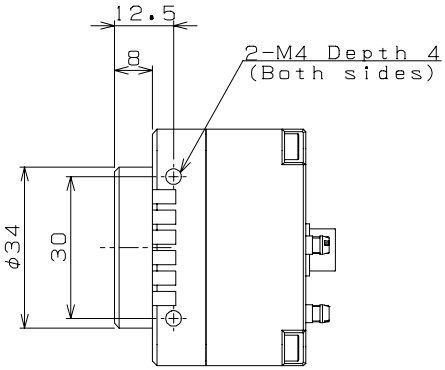


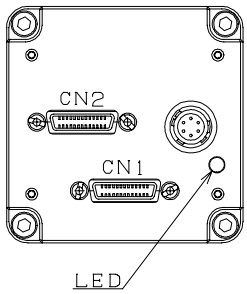
D. Dimensions





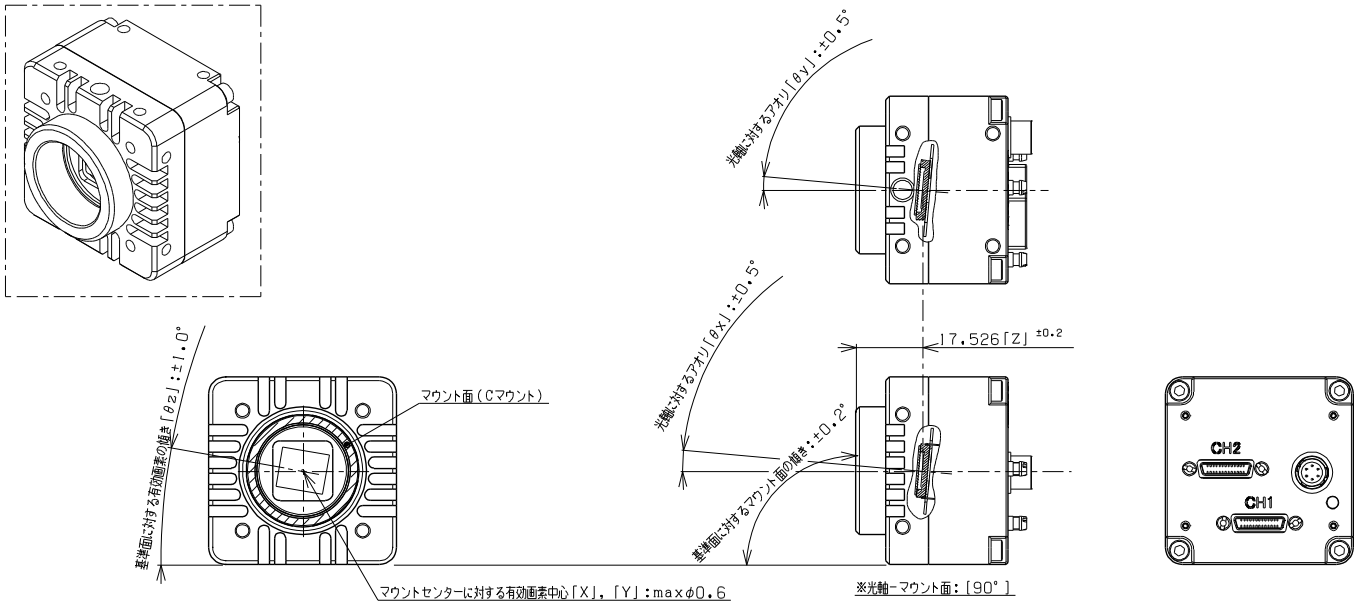


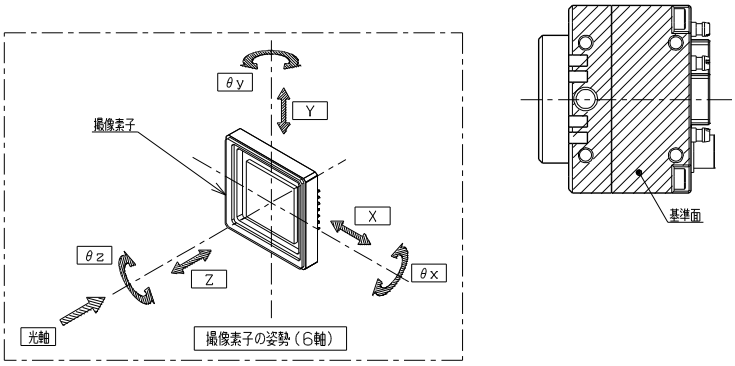




Unit: mm

E. Accuracy of Sensor Position





Unit: mm

III. Camera Installation

During installation of the camera, the following equipment is required:

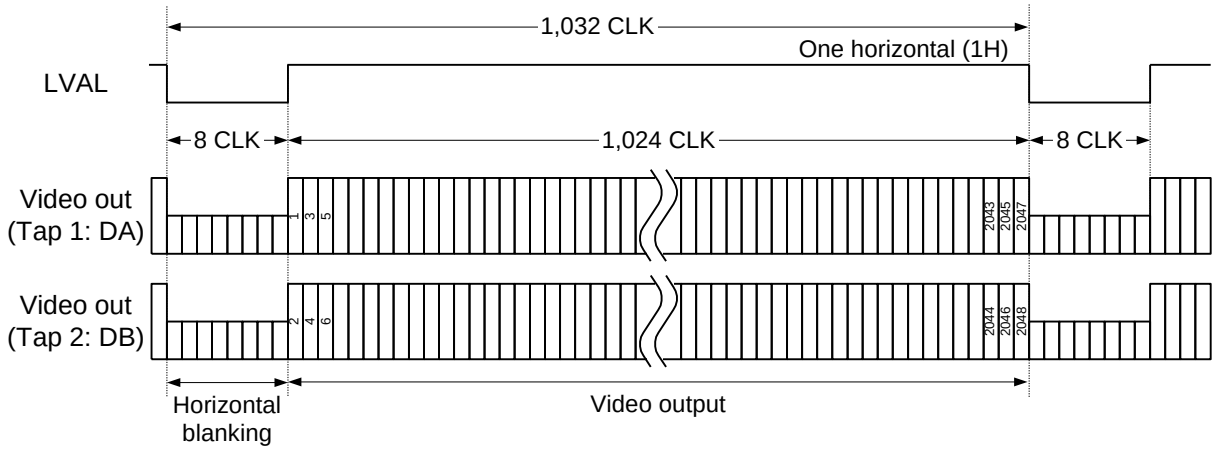
- Control Software or Serial Communication Software to access the camera register. For more information on using the software, please refer to Section VII: Control Software. For more information on accessing the camera register, please refer to Section VI: The Communication Protocol Specifications.
- Camera Link Cable x 2 (SDR Connector : Camera Side)
  When using on Full Configuration Settings, please insure the cable is up to the qualification required.
- The Frame Grabber being used should be able to support Full, Medium, and Base Camera Link Configuration. When using the PoCL function, please insure the Frame Grabber supports PoCL.

IV. Camera Output Timing Charts

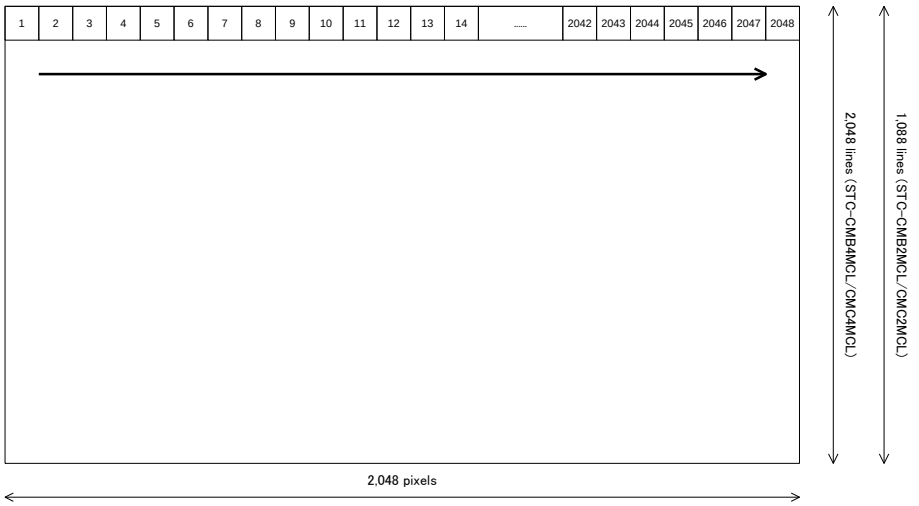
A. Horizontal Timing (STC-CMB2MCL / STC-CMC2MCL / STC-CMB4MCL / STC-CMC4MCL

1. 2 TAPS (1X2-1Y) / Horizontal 2,048 Pixels

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The Pixel Order for the Image

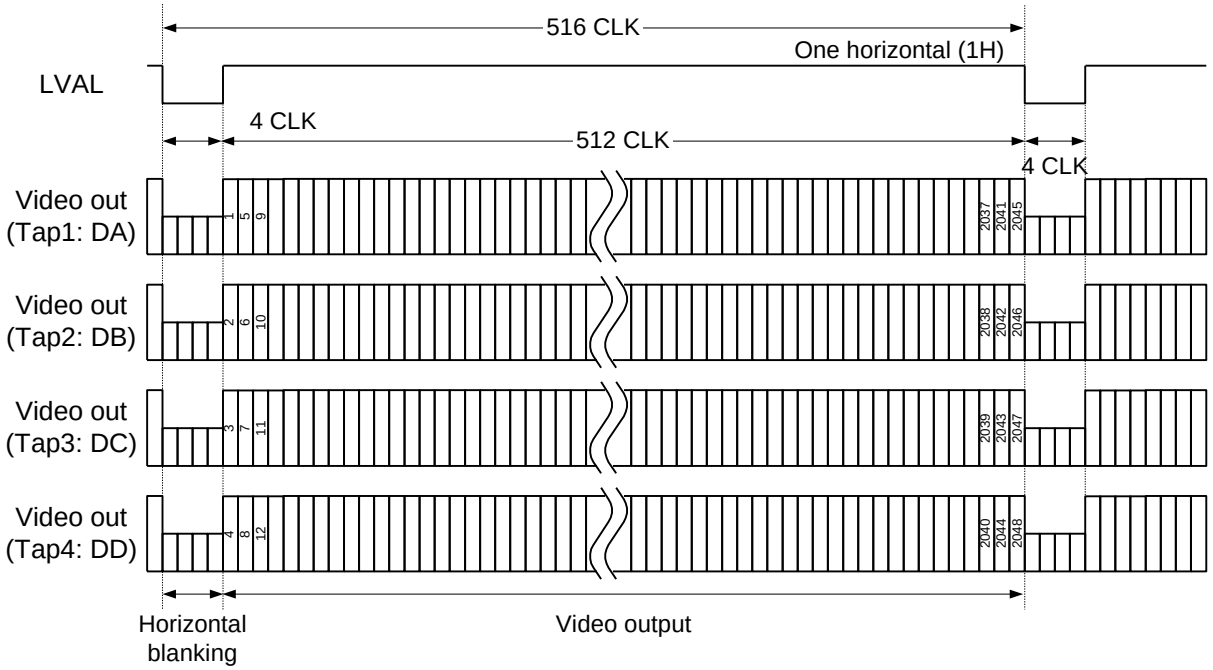


TAP1: DA Output Pixels

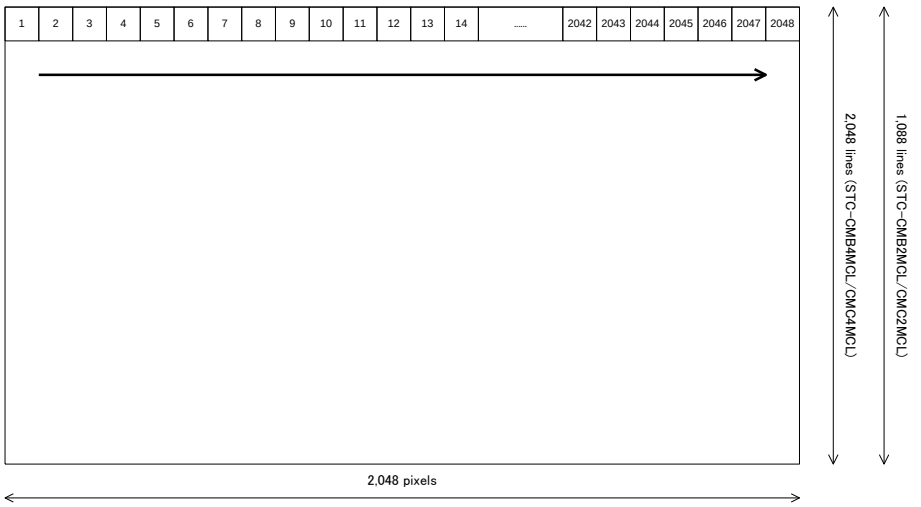
TAP2: DB Output Pixels

2. 4 Taps (1X4-1Y) / Horizontal 2,048 Pixels

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds(42.5MHz)



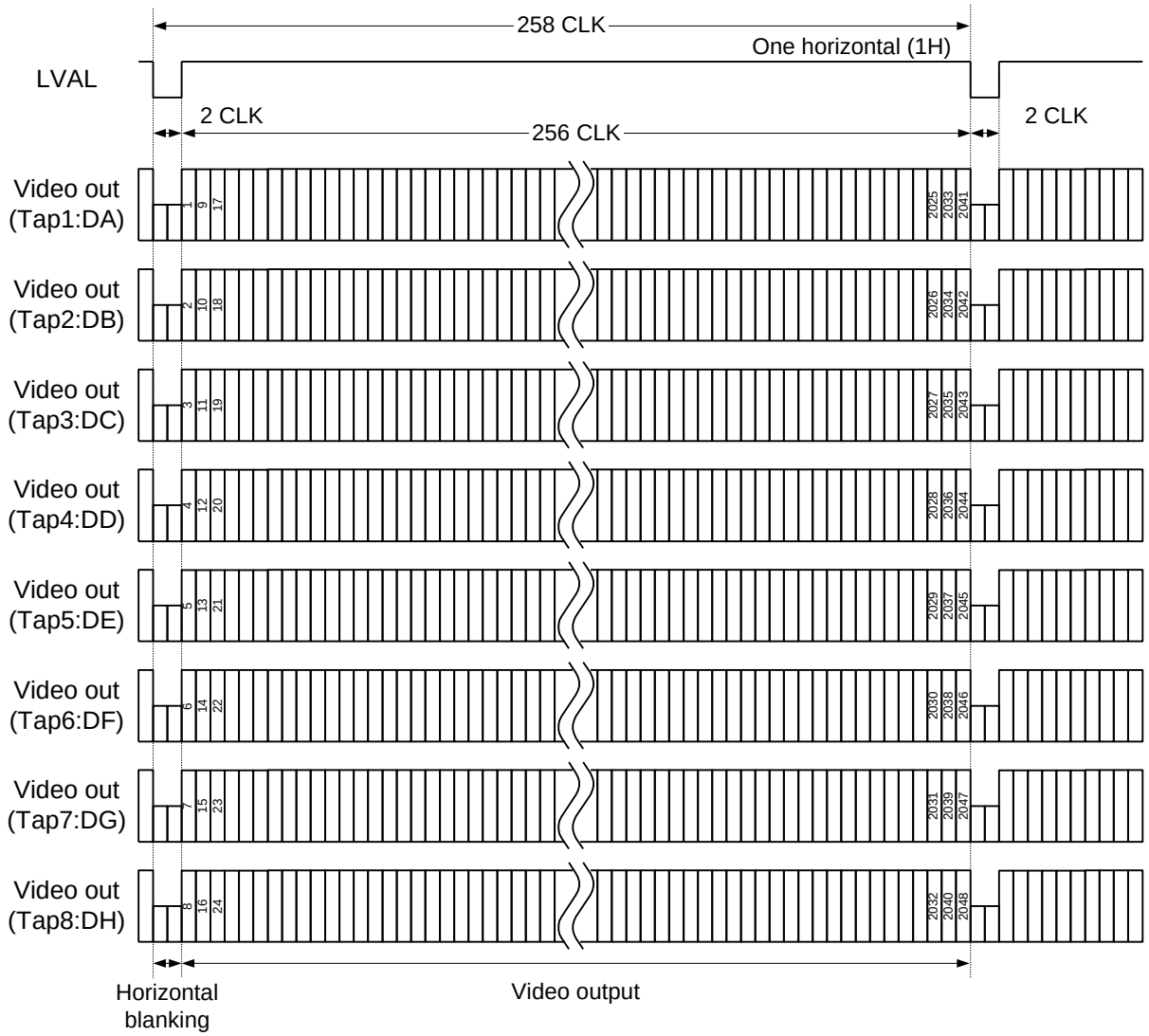
The pixel order for the Image



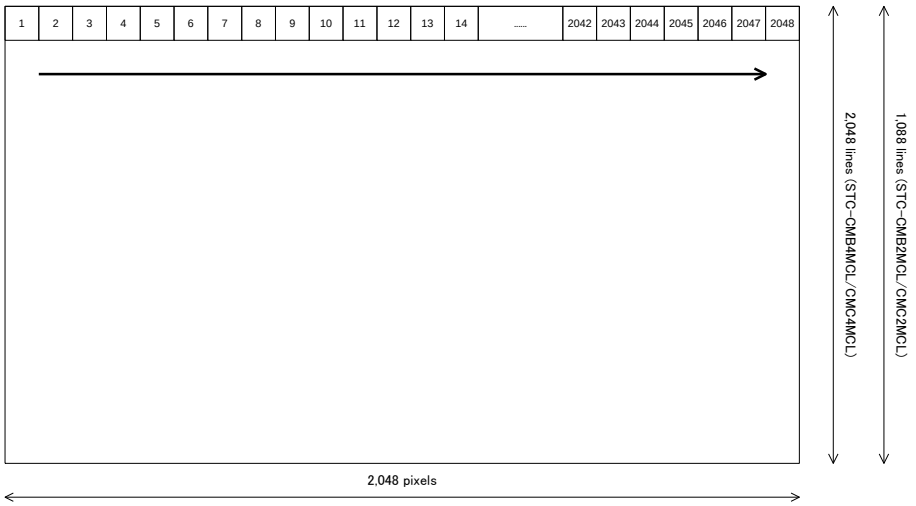
TAP1: DA output pixels	1	5	9	13	17	21	25	.....	2021	2025	2029	2033	2037	2041	2045
TAP2: DB output pixels	2	6	10	14	18	22	26	.....	2022	2026	2030	2034	2038	2042	2046
TAP3: DC output pixels	3	7	11	15	19	23	27	.....	2023	2027	2031	2035	2039	2043	2047
TAP4: DD output pixels	4	8	12	16	20	24	28	.....	2024	2028	2032	2036	2040	2044	2048

3. 8 Taps (1X8-1Y) / Horizontal 2,048 Pixels

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



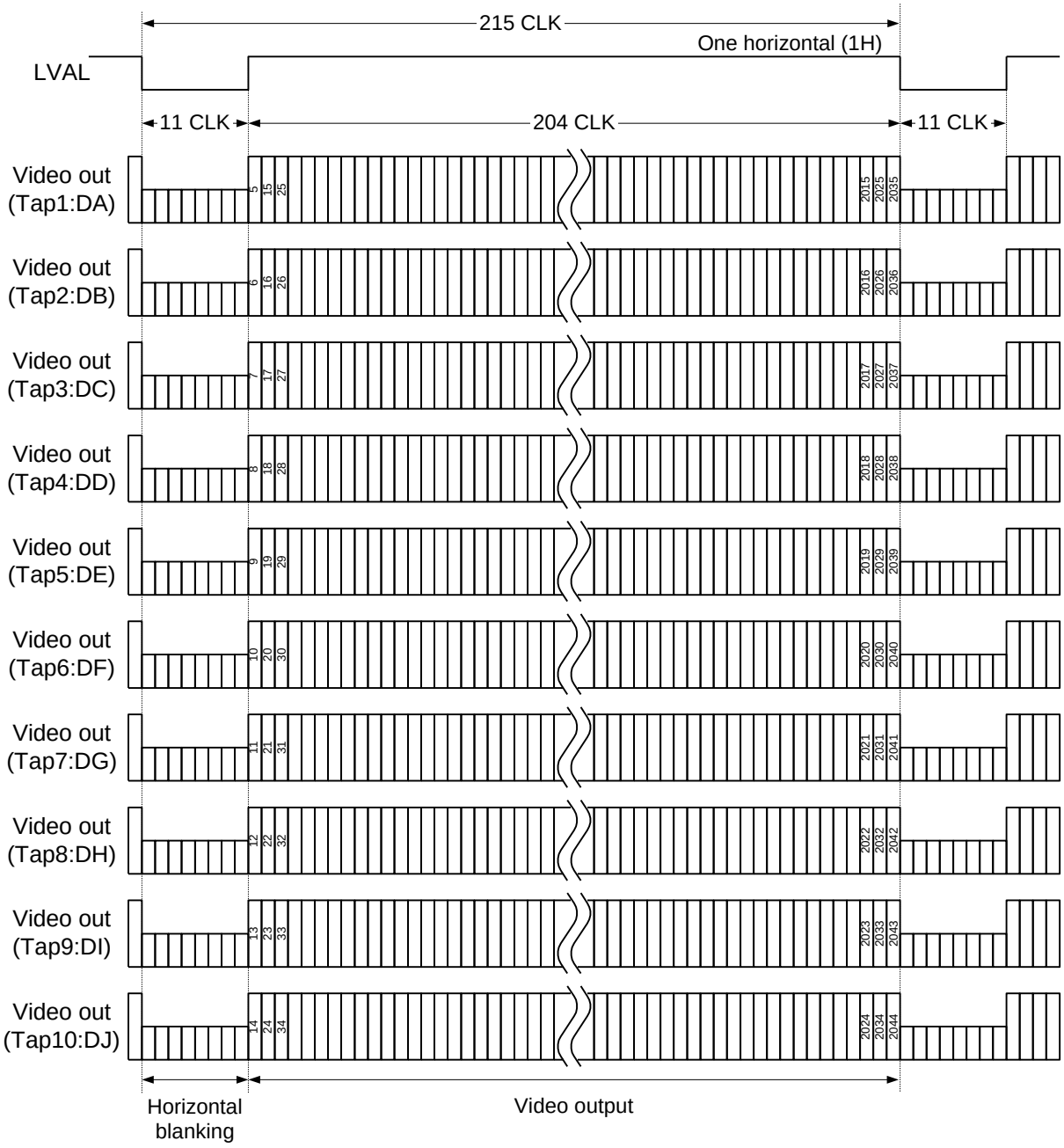
The pixel order for the Image



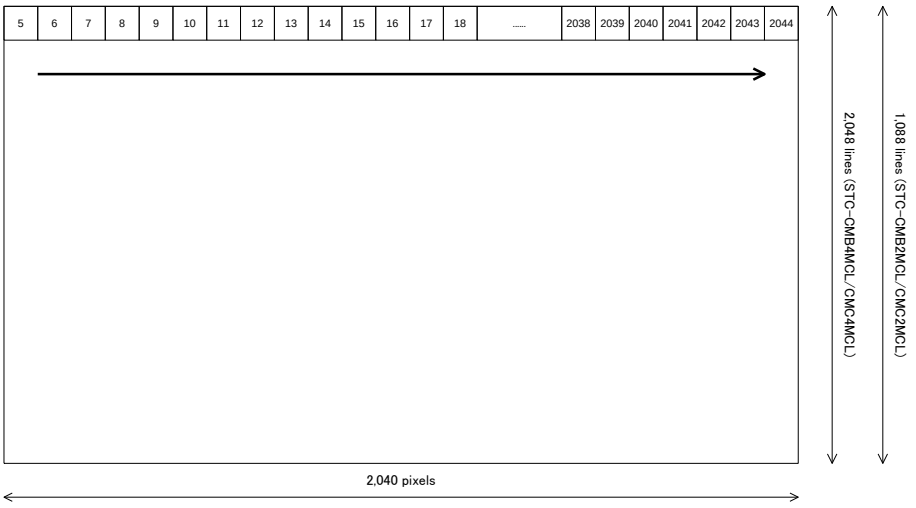
TAP1: DA output pixels	1	9	17	25	33	41	49	.....	1993	2001	2009	2017	2025	2033	2041
TAP2: DB output pixels	2	10	18	26	34	42	50	.....	1994	2002	2010	2018	2026	2034	2042
TAP3: DC output pixels	3	11	19	27	35	43	51	.....	1995	2003	2011	2019	2027	2035	2043
TAP4: DD output pixels	4	12	20	28	36	44	52	.....	1996	2004	2012	2020	2028	2036	2044
TAP5: DE output pixels	5	13	21	29	37	45	53	.....	1997	2005	2013	2021	2029	2037	2045
TAP6: DF output pixels	6	14	22	30	38	46	54	.....	1998	2006	2014	2022	2030	2038	2046
TAP7: DG output pixels	7	15	23	31	39	47	55	.....	1999	2007	2015	2023	2031	2039	2047
TAP8: DH output pixels	8	16	24	32	40	48	56	.....	2000	2008	2016	2024	2032	2040	2048

4. 10 TAPS (1X10-1Y) / Horizontal 2,040 Pixels

1 CLK = 12.500 nseconds (80MHz)
1 CLK = 25.000 nseconds (40MHz)



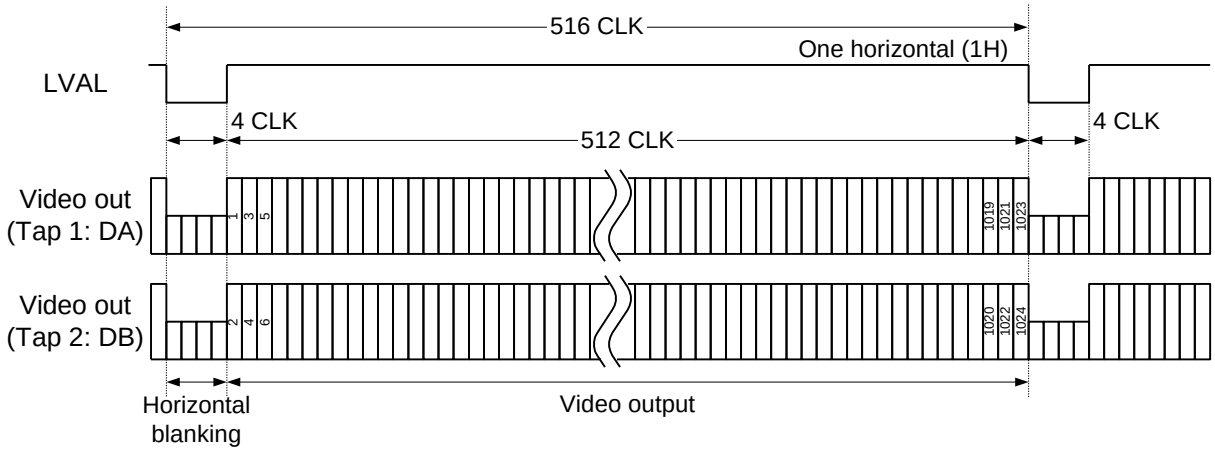
The pixel order for the Image



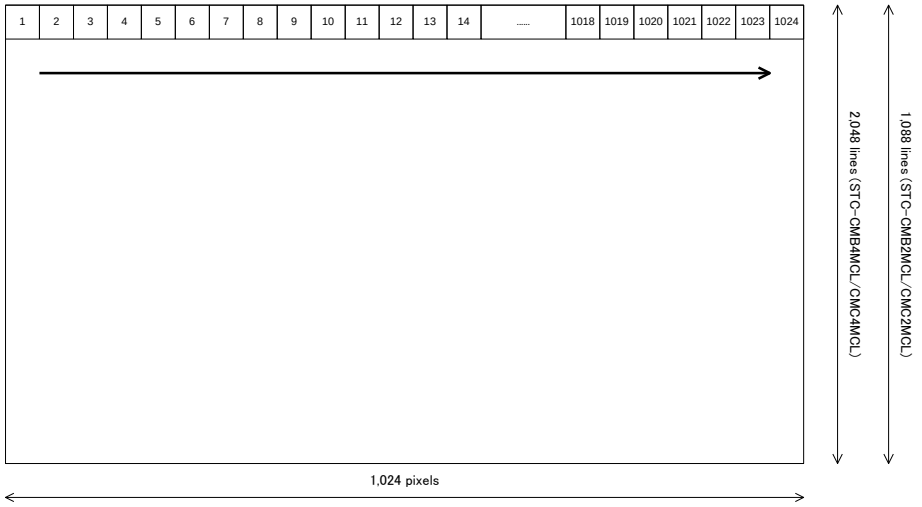
TAP1: DA output pixels	5	15	25	35	45	55	65	.....	1975	1985	1995	2005	2015	2025	2035
TAP2: DB output pixels	6	16	26	36	46	56	66	.....	1976	1986	1996	2006	2016	2026	2036
TAP3: DC output pixels	7	17	27	37	47	57	67	.....	1977	1987	1997	2007	2017	2027	2037
TAP4: DD output pixels	8	18	28	38	48	58	68	.....	1978	1988	1998	2008	2018	2028	2038
TAP5: DE output pixels	9	19	29	39	49	59	69	.....	1979	1989	1999	2009	2019	2029	2039
TAP6: DF output pixels	10	20	30	40	50	60	70	.....	1980	1990	2000	2010	2020	2030	2040
TAP7: DG output pixels	11	21	31	41	51	61	71	.....	1981	1991	2001	2011	2021	2031	2041
TAP8: DH output pixels	12	22	32	42	52	62	72	.....	1982	1992	2002	2012	2022	2032	2042
TAP9: DG output pixels	13	23	33	43	53	63	73	.....	1983	1993	2003	2013	2023	2033	2043
TAP10: DH output pixels	14	24	34	44	54	64	74	.....	1984	1994	2004	2014	2024	2034	2044

5. 2 Taps (1X2-1Y) / Horizontal 1,024 Pixels

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image



TAP1: DA output pixels

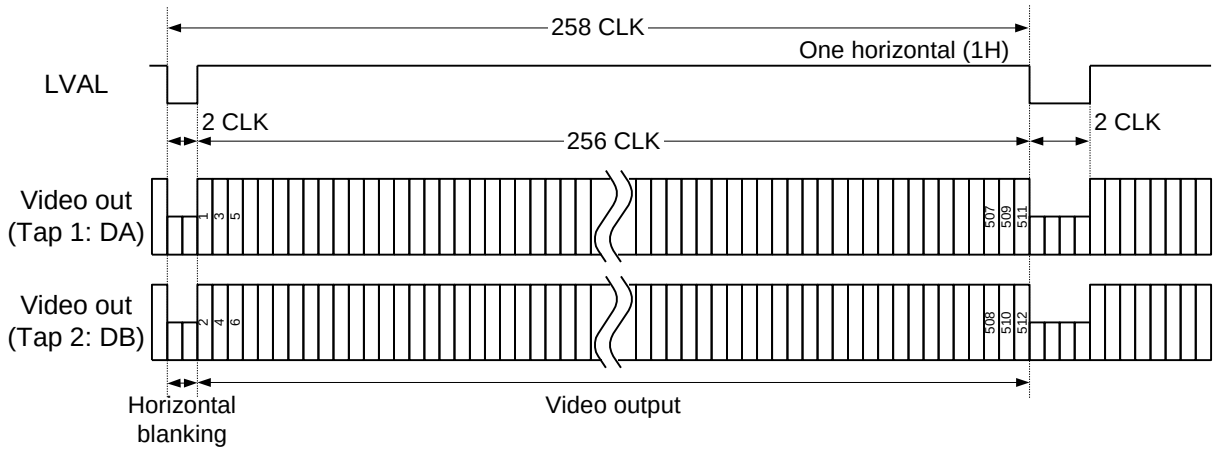
1	3	5	7	9	11	13	.....	1011	1013	1015	1017	1019	1021	1023

TAP2: DB output pixels

2	4	6	8	10	12	14	.....	1012	1014	1016	1018	1020	1022	1024

6. 2 Taps (1X2-1Y) / Horizontal 512 Pixels

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image

TAP1: DA output pixels

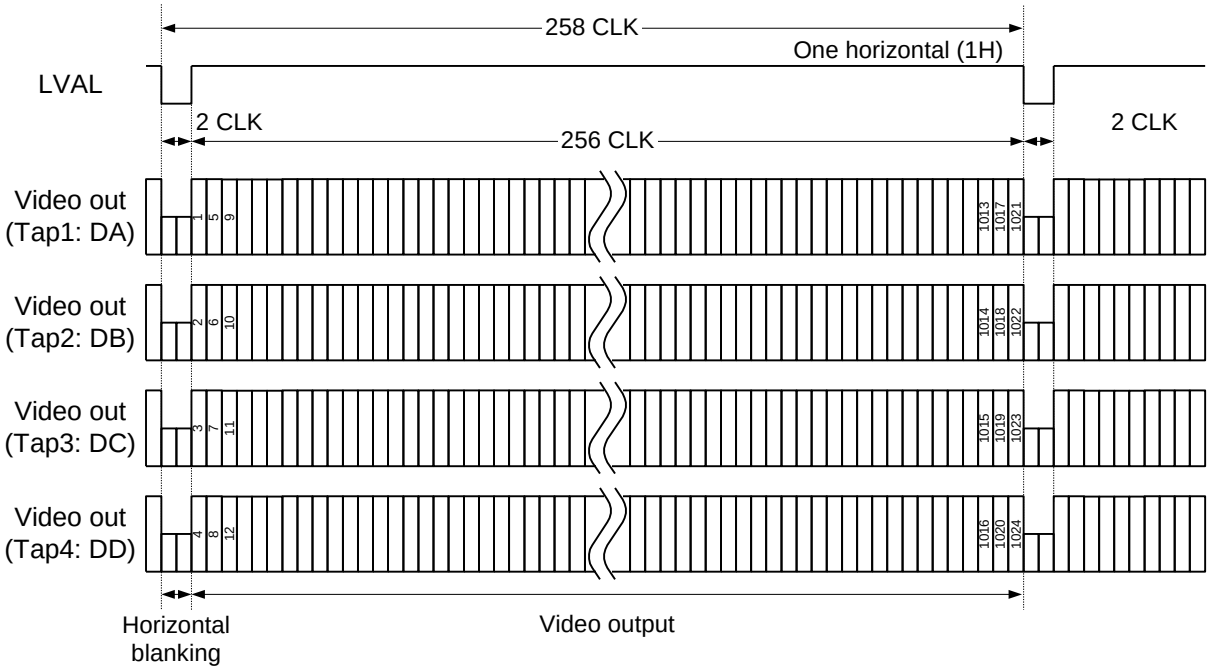
1	3	5	7	9	11	13	.....	499	501	503	505	507	509	511

TAP2: DB output pixels

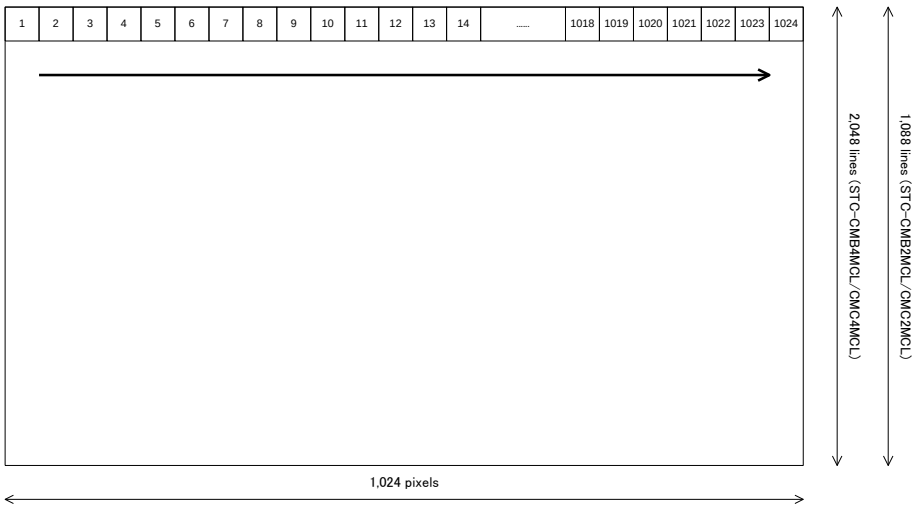
2	4	6	8	10	12	14	.....	500	502	504	506	508	510	512

7. 4 Taps / Horizontal 1,024 Pixels

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



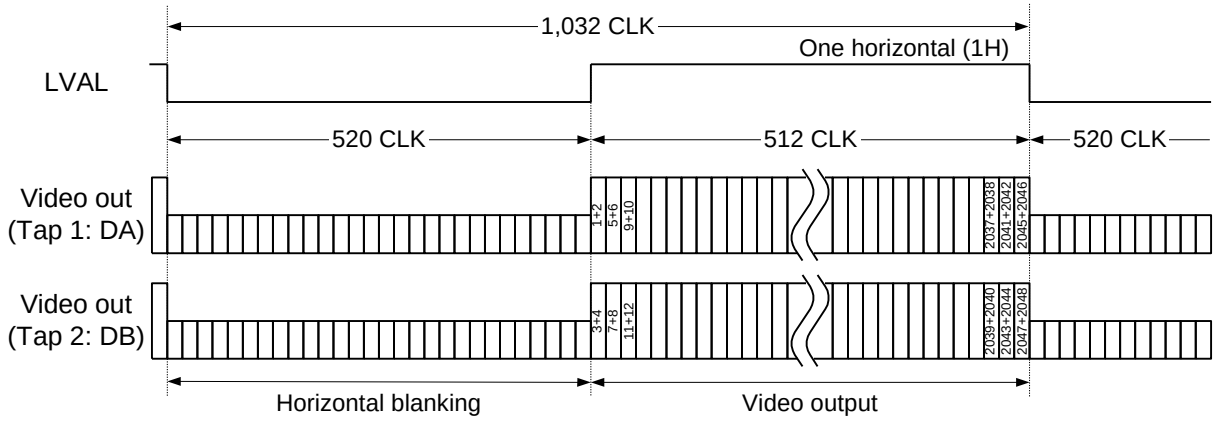
The pixel order for the Image



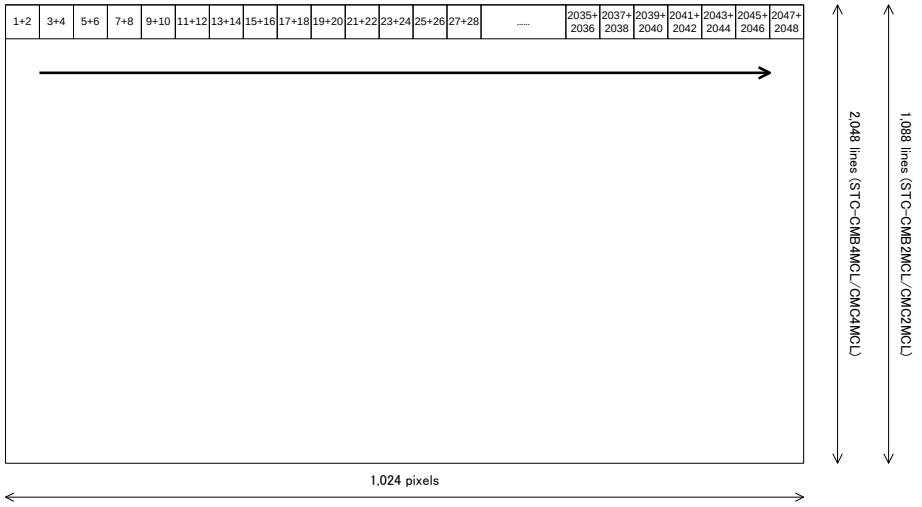
TAP1: DA output pixels	1	5	9	13	17	21	25	.....	997	1001	1005	1009	1013	1017	1021
TAP2: DB output pixels	2	6	10	14	18	22	26	.....	998	1002	1006	1010	1014	1018	1022
TAP3: DC output pixels	3	7	11	15	19	23	27	.....	999	1003	1007	1011	1015	1019	1023
TAP4: DD output pixels	4	8	12	16	20	24	28	.....	1000	1004	1008	1012	1016	1020	1024

8. 2 Taps (1X2-1Y) / 2 x 2 Binning

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image

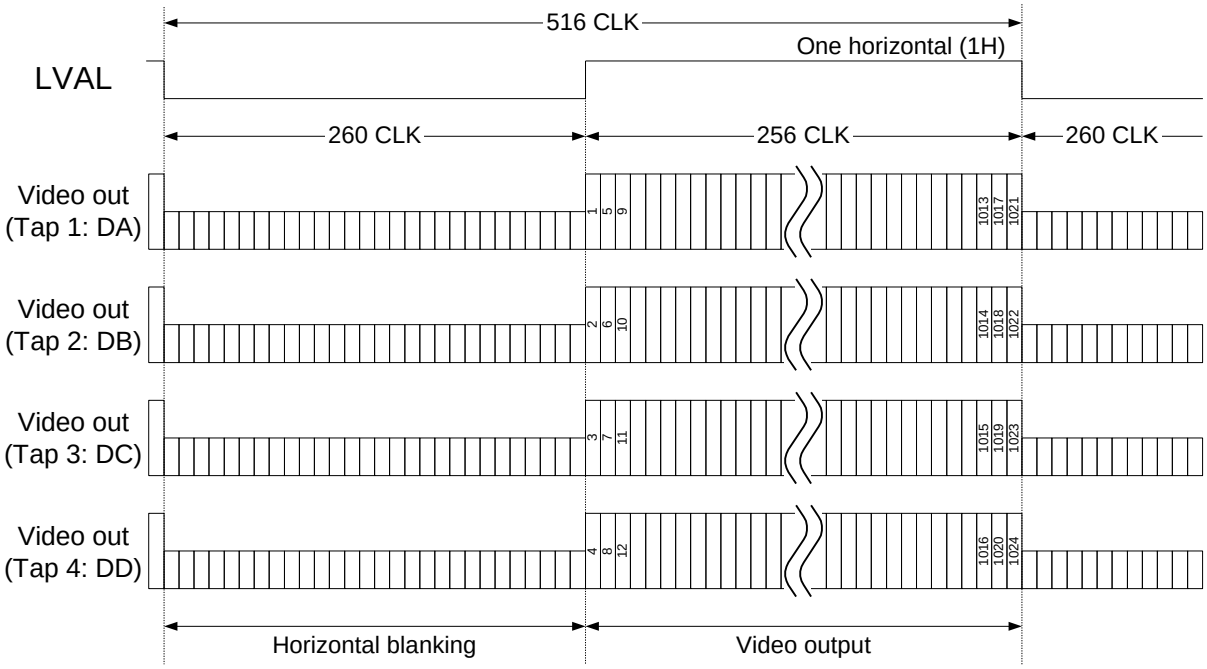


TAP1: DA output pixels

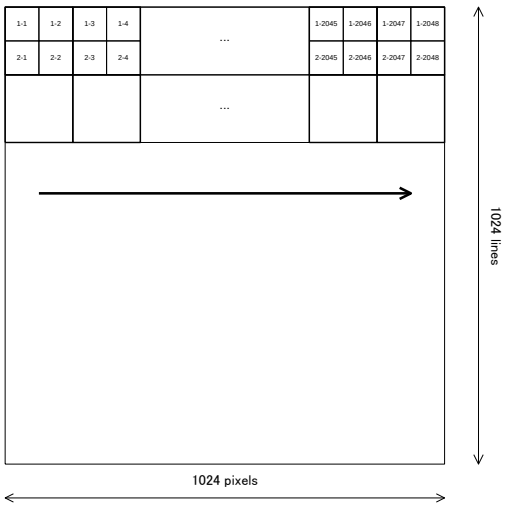
TAP2: DB output pixels

9. 4 Taps (1X4-1Y) / 2 x 2 Binning

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



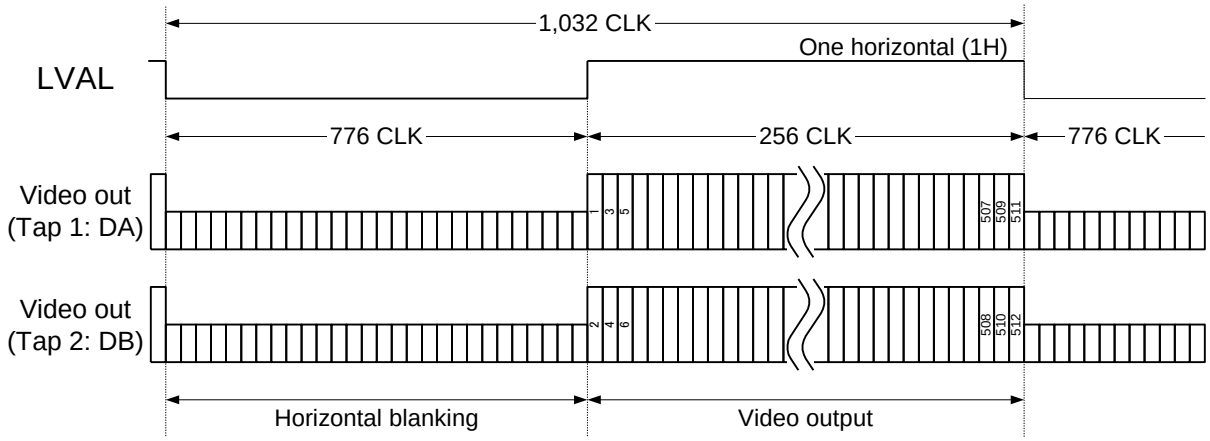
The pixel order for the Image



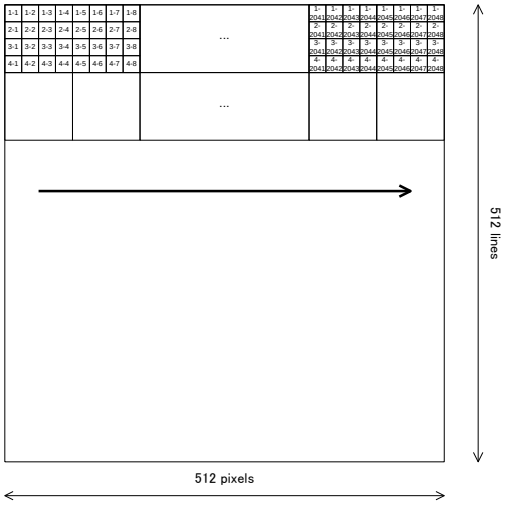
TAP1: DA output pixels	1	5	9	13	17	21	25	.....	997	1001	1005	1009	1013	1017	1021
TAP2: DB output pixels	2	6	10	14	18	22	26	.....	998	1002	1006	1010	1014	1018	1022
TAP3: DC output pixels	3	7	11	15	19	23	27	.....	999	1003	1007	1011	1015	1019	1023
TAP4: DD output pixels	4	8	12	16	20	24	28	.....	1000	1004	1008	1012	1016	1020	1024

10. 2 Taps (1X2-1Y) / 4 x 4 Binning

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image



TAP1: DA output pixels

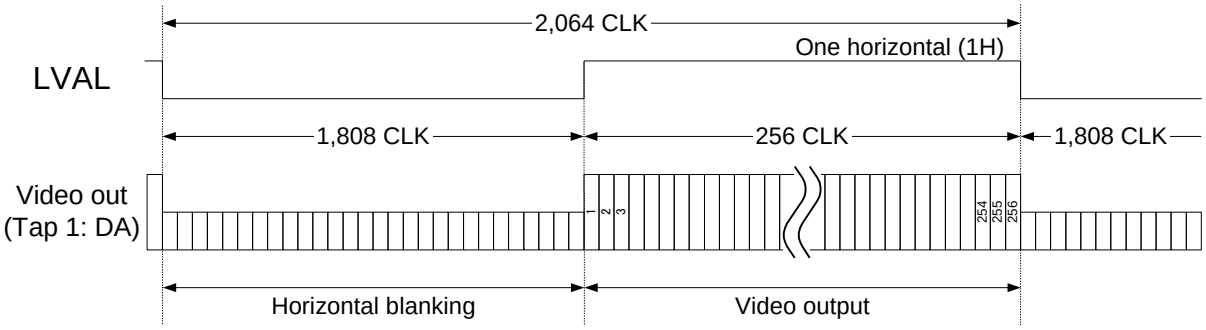
1	3	5	7	9	11	13	.....	499	501	503	505	507	509	511

TAP2: DB output pixels

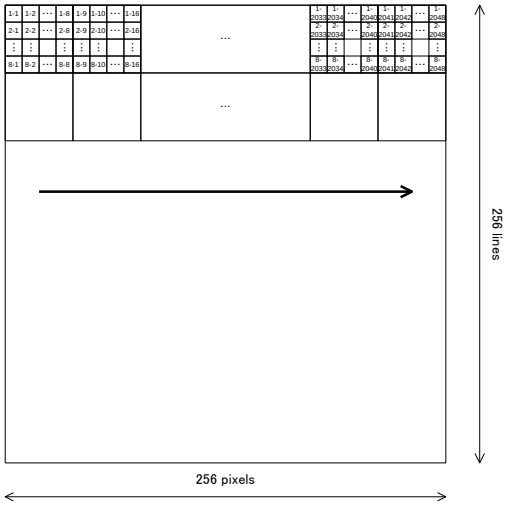
2	4	6	8	10	12	14	.....	500	502	504	506	508	510	512

11. 1 Taps (1X-1Y) / 8 x 8 Binning

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image

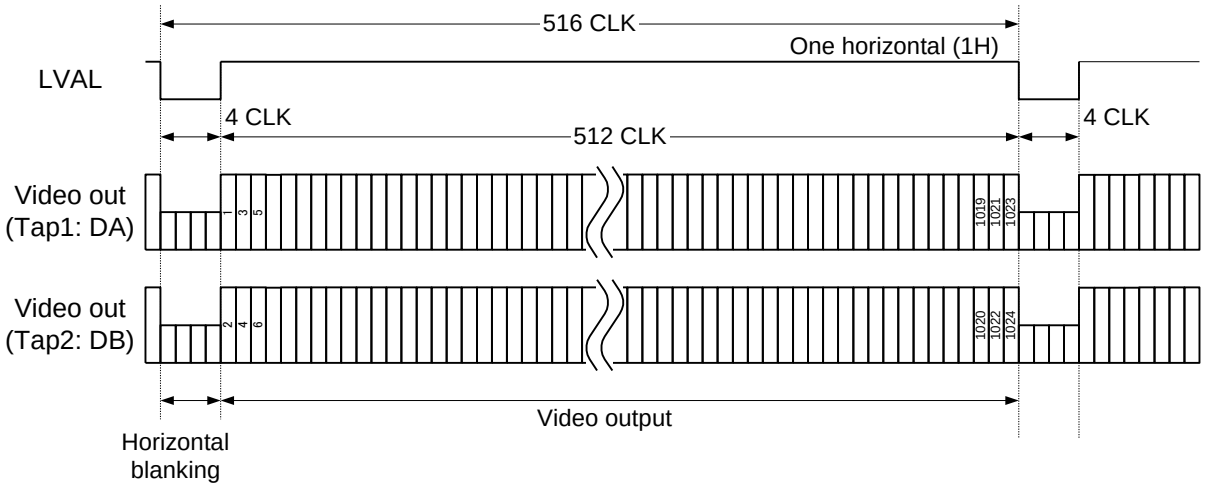


TAP1: DA output pixels

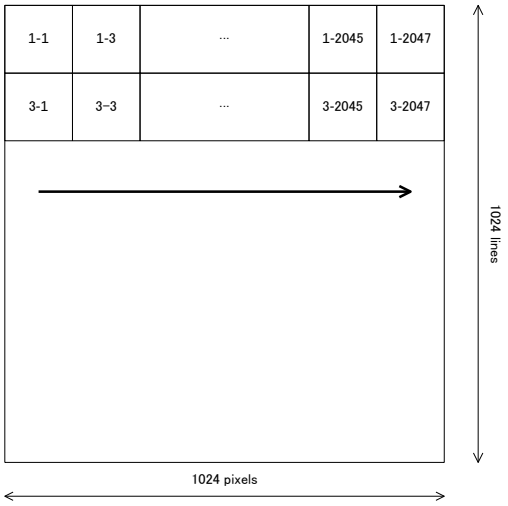
1	2	3	4	5	6	7	.....	250	251	252	253	254	255	256

12. 2 Taps (1X2-1Y) / 2 x 2 Subsampling

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image



TAP1: DA output pixels

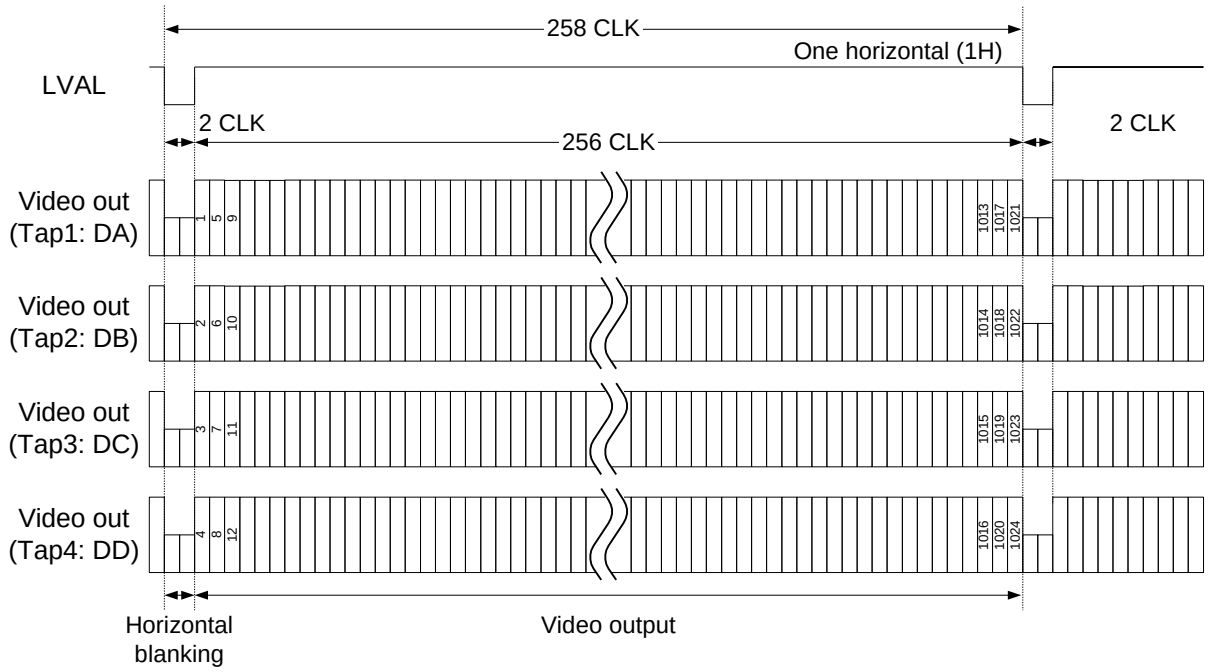
1	3	5	7	9	11	13	.....	1011	1013	1015	1017	1019	1021	1023

TAP2: DB output pixels

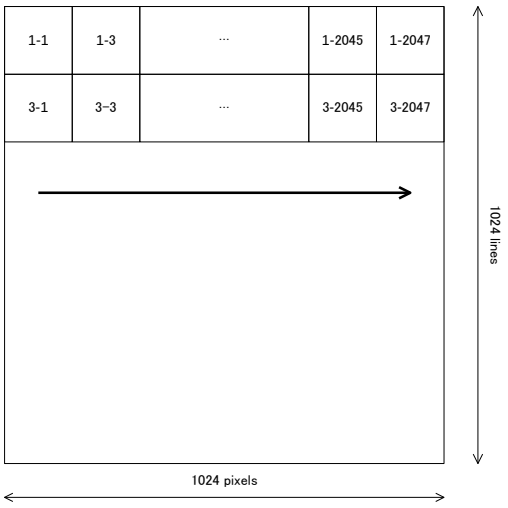
2	4	6	8	10	12	14	.....	1012	1014	1016	1018	1020	1022	1024

13. 4 Taps (1X4-1Y) / 2 x 2 Subsampling

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image



TAP1: DA output pixels

1	5	9	13	17	21	25	.....	997	1001	1005	1009	1013	1017	1021

TAP2: DB output pixels

2	6	10	14	18	22	26	.....	998	1002	1006	1010	1014	1018	1022

TAP3: DC output pixels

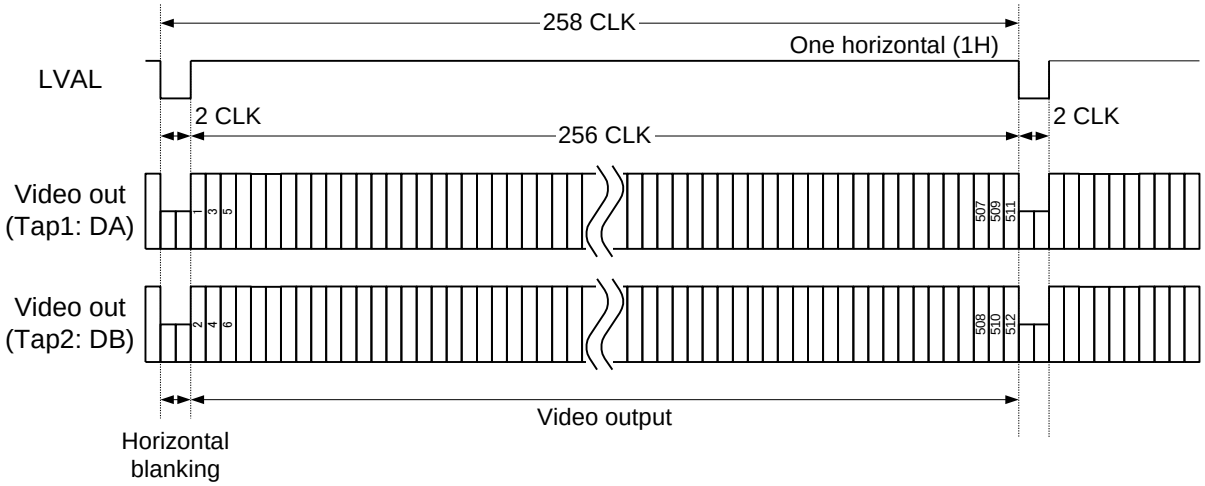
3	7	11	15	19	23	27	.....	999	1003	1007	1011	1015	1019	1023

TAP4: DD output pixels

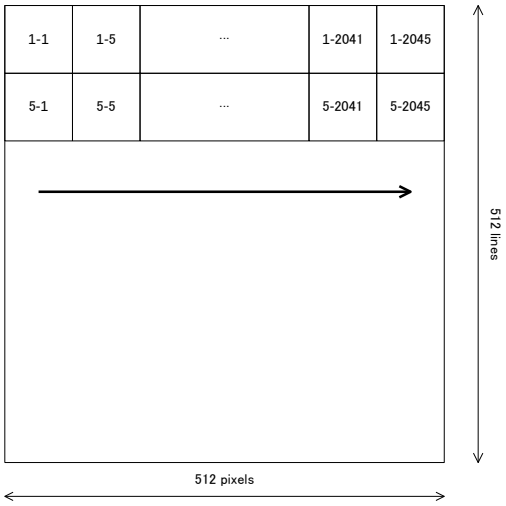
4	8	12	16	20	24	28	.....	1000	1004	1008	1012	1016	1020	1024

14. 2 Taps (1X2-1Y) / 4 x 4 Subsampling

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image



TAP1: DA output pixels

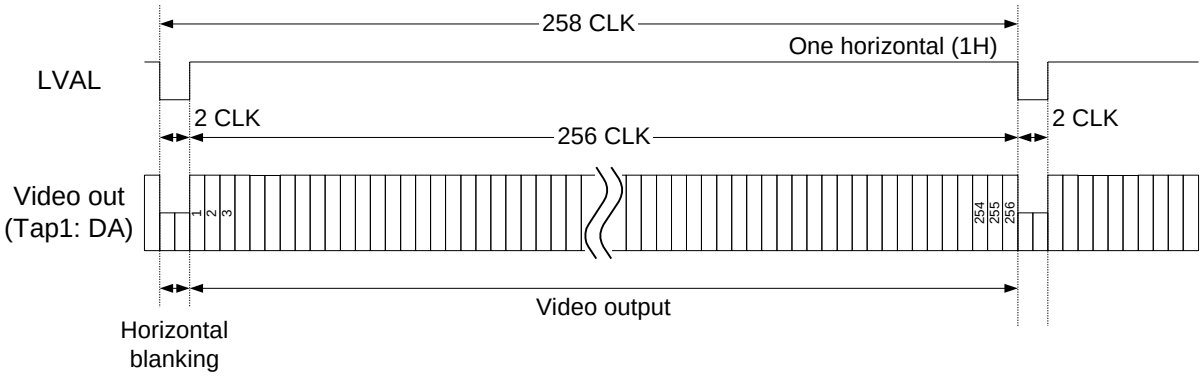
1	3	5	7	9	11	13	.....	499	501	503	505	507	509	511

TAP2: DB output pixels

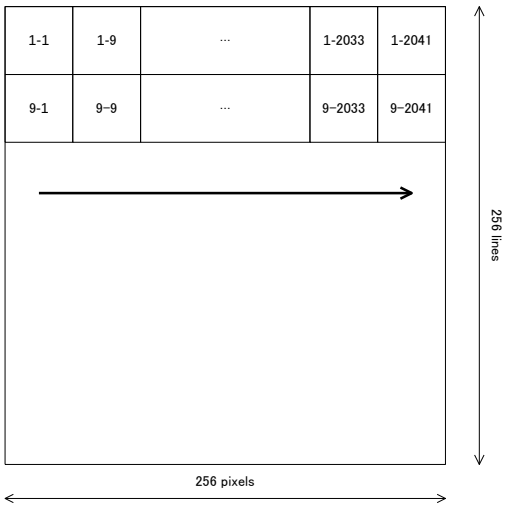
2	4	6	8	10	12	14	.....	500	502	504	506	508	510	512

15. 1 Taps (1X-1Y) / 8 x 8 Subsampling

1 CLK = 11.764 nseconds (85MHz)
1 CLK = 23.524 nseconds (42.5MHz)



The pixel order for the Image



TAP1: DA output pixels

1	2	3	4	5	6	7	.....	250	251	252	253	254	255	256

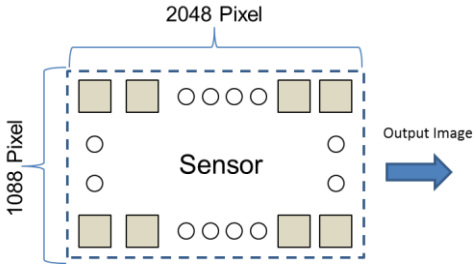
B. Vertical Timing

Three video scan modes exist. The details for these three scan modes are described below.

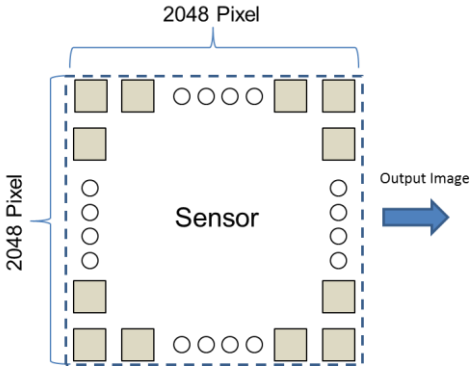
Full Scan: All of the lines and pixels output from the camera.
Binning: Averaged pixel value output from the camera.
Subsampling: Skipped the lines and pixels output from the camera.

Overview of Full Scan Mode:

All of the lines and pixels are output and the entire image is shown. For transmitting the image, some configurations will not be supported, or can drop the frame rate.



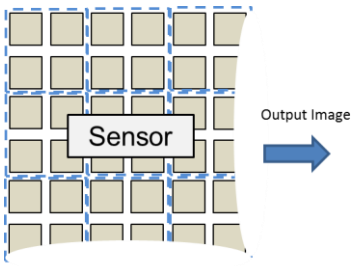
Output image from Sensor on Fullscan (STC-CMB2MCL / CMC2MCL)



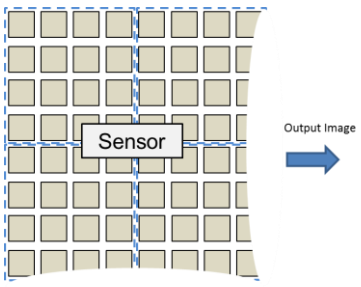
Output image from Sensor on Fullscan (STC-CMB4MCL / CMC4MCL)

Overview of Binning (2MP / 4MP):

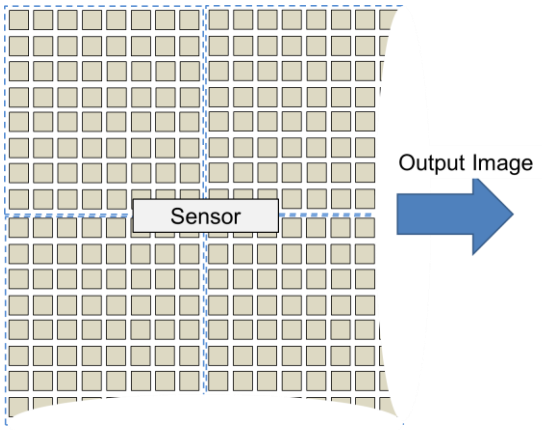
In this mode, the data of a group of pixels is averaged and output as a one pixel data. Therefore, by selecting this mode the user can reduce the noise level. In this case, instead of averaging the pixel, all subjected pixels black level will increase and affect the dynamic range. Because of this reason, we implemented the averaging method. Resolution will decrease due to the lower number of pixels, however the user can achieve a better noise level and higher frame rate compared to full scan mode.



Output image from Sensor on 2 x 2 Binning.



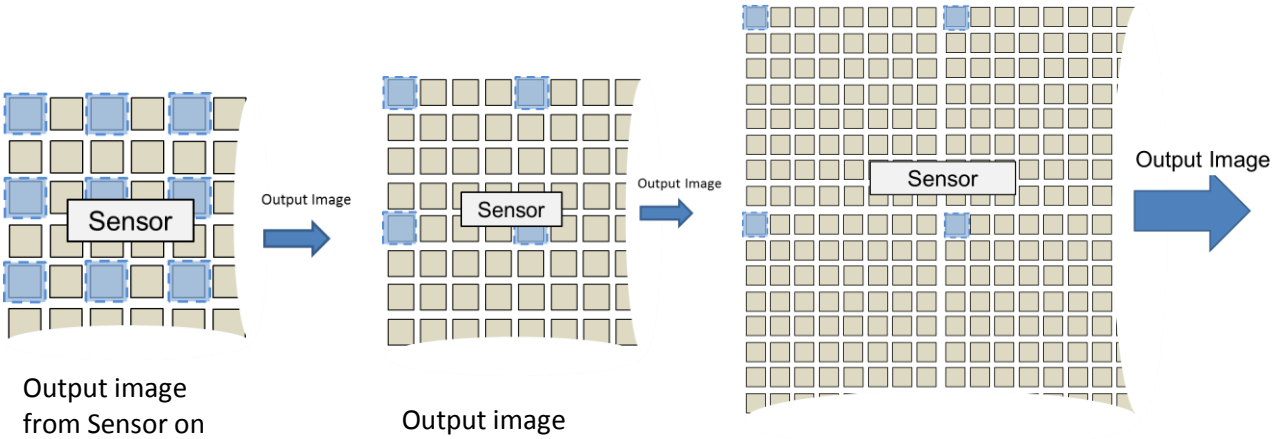
Output image from Sensor on 4 x 4 Binning.



Output image from Sensor on 8 x 8 Binning.

Overview of Subsampling (2MP / 4MP):

In Subsampling mode, lines and pixel values skip, then are output in the shown image. For skipped pixel data, the resolution will decrease while the frame rate increases. Subsampling reduces the output resolution and increases the frame rate without the FOV (Field of View).



Output image from Sensor on 2 x 2 Subsampling.

Output image from Sensor on 4 x 4 Subsampling.

Output image from Sensor on 8 x 8 Subsampling.

1. Full Scan (STC-CMB2MCL / CMC2MCL)

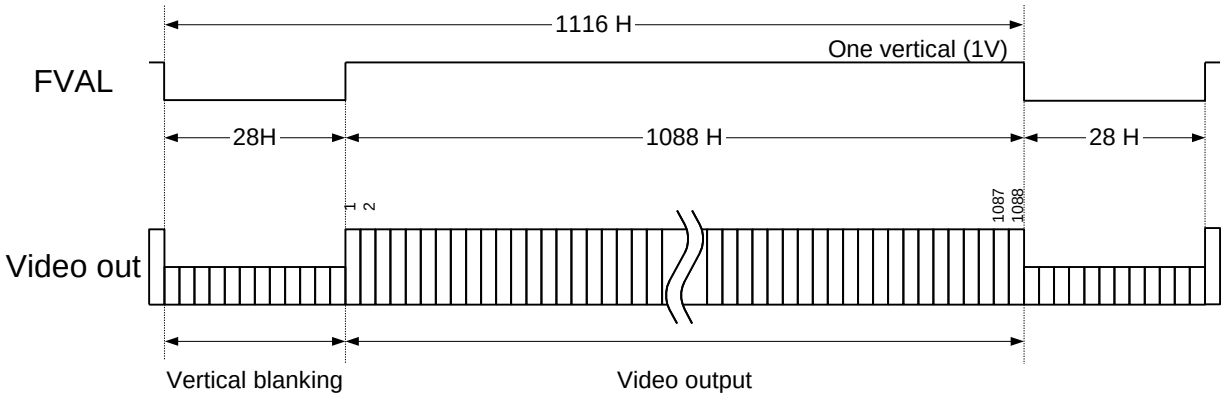


Table of Video Output on Full Scan Mode (STC-CMB2MCL / CMC2MCL)

Mode (29H)	Tap Number	Configuration	CameraLink Output PixelClock Frequency(MHz)	Horizontal Pixel (Pixel)	Sensor Output Pixel Clock(MHz)	FPS	Camera Link Output Bit
0	2	Base	85.0	2048	10.625	73.8	8/10
5	2	Base	85.0	1024	21.250	147.6	8/10
6	2	Base	85.0	512	42.500	295.2	8/10
1	4	MEDIUM	85.0	2048	21.250	147.6	8/10
7	4	MEDIUM	85.0	1024	42.500	295.2	8/10
2	8	FULL	85.0	2048	42.500	295.2	8
3	10		80.0	2040	48.000	333.4	8
0	2	Base	42.5	2048	5.313	36.9	8/10
5	2	Base	42.5	1024	10.625	73.8	8/10
6	2	Base	42.5	512	21.250	147.6	8/10
1	4	MEDIUM	42.5	2048	10.625	73.8	8/10
7	4	MEDIUM	42.5	1024	21.250	147.6	8/10
2	8	FULL	42.5	2048	21.250	147.6	8
3	10		40.0	2040	24.000	166.7	8

2. Full Scan (STC-CMB4MCL / CMC4MCL)

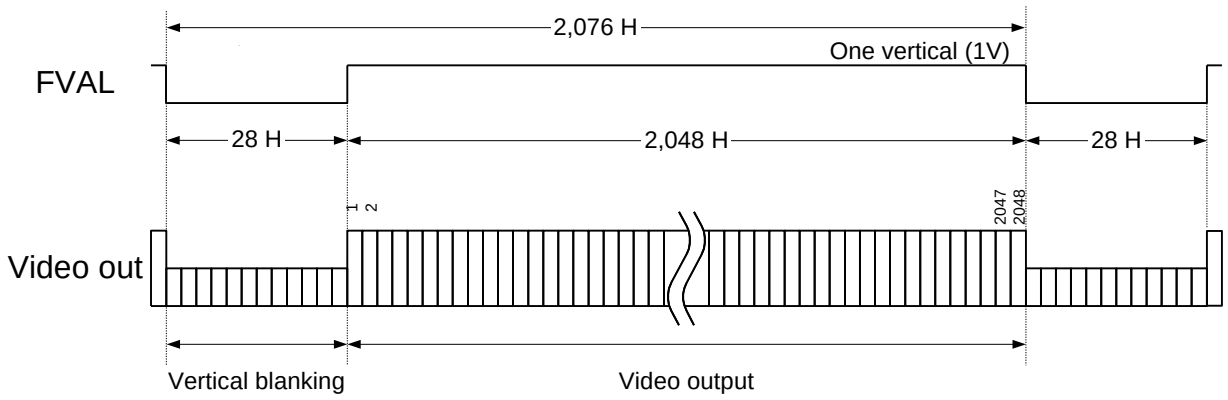
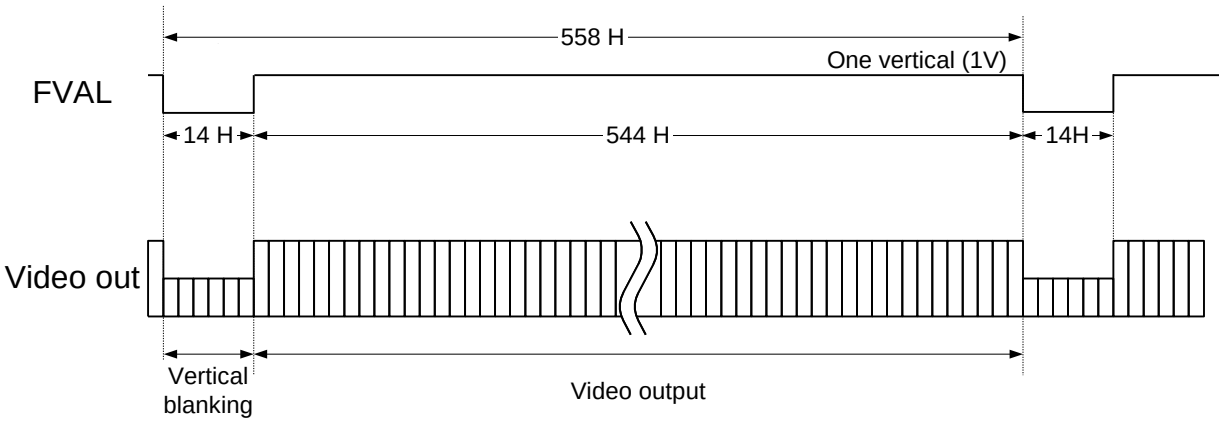


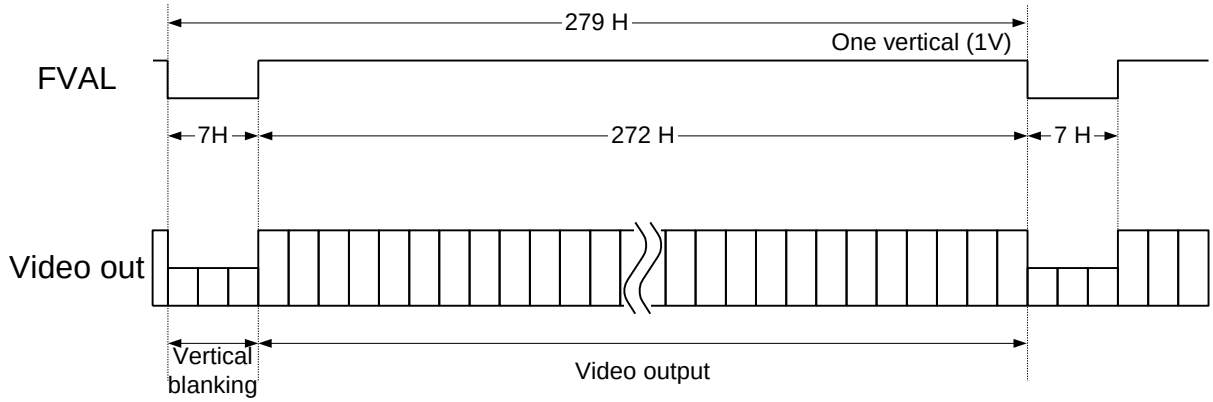
Table of Video Output on Full Scan Mode (STC-CMB4MCL / CMC4MCL)

Mode (29H)	Tap Number	Configuration	CameraLink Output PixelClock Frequency(MHz)	Horizontal Pixel (Pixel)	Sensor Output Pixel Clock(MHz)	FPS	Camera Link Output Bit
0	2	Base	85.0	2048	10.625	39.7	8/10
5	2	Base	85.0	1024	21.250	79.3	8/10
6	2	Base	85.0	512	42.500	158.7	8/10
1	4	Medium	85.0	2048	21.250	79.3	8/10
7	4	Medium	85.0	1024	42.500	158.7	8/10
2	8	Full	85.0	2048	42.500	158.7	8
3	10		80.0	2040	48.000	179.2	8
0	2	Base	42.5	2048	5.313	19.8	8/10
5	2	Base	42.5	1024	10.625	39.7	8/10
6	2	Base	42.5	512	21.250	79.3	8/10
1	4	Medium	42.5	2048	10.625	39.7	8/10
7	4	Medium	42.5	1024	21.250	79.3	8/10
2	8	Full	42.5	2048	21.250	79.3	8
3	10		40.0	2040	24.000	89.6	8

3. 2 x 2 Binning (STC-CMB2MCL / CMC2MCL)



4. 4 x 4 Binning (STC-CMB2MCL / CMC2MCL)



5. 8 x 8 Binning (STC-CMB2MCL / CMC2MCL)

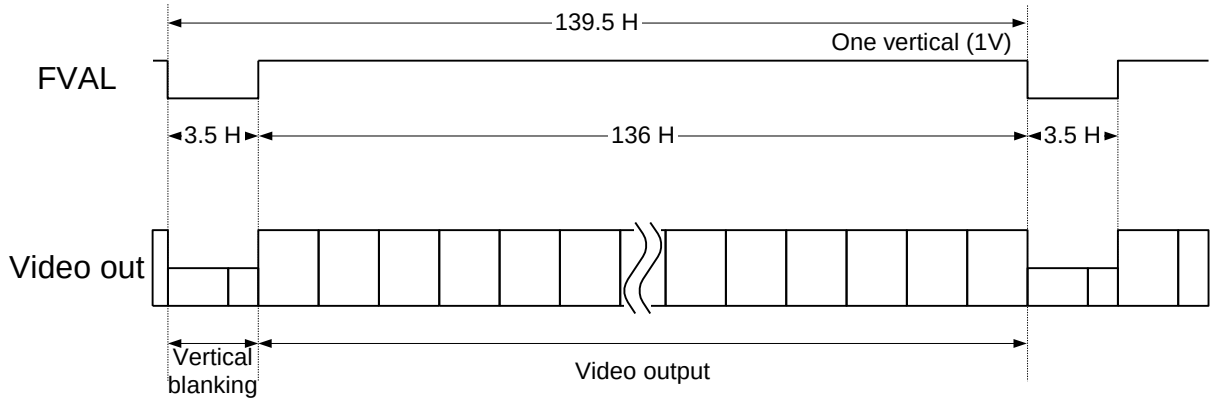
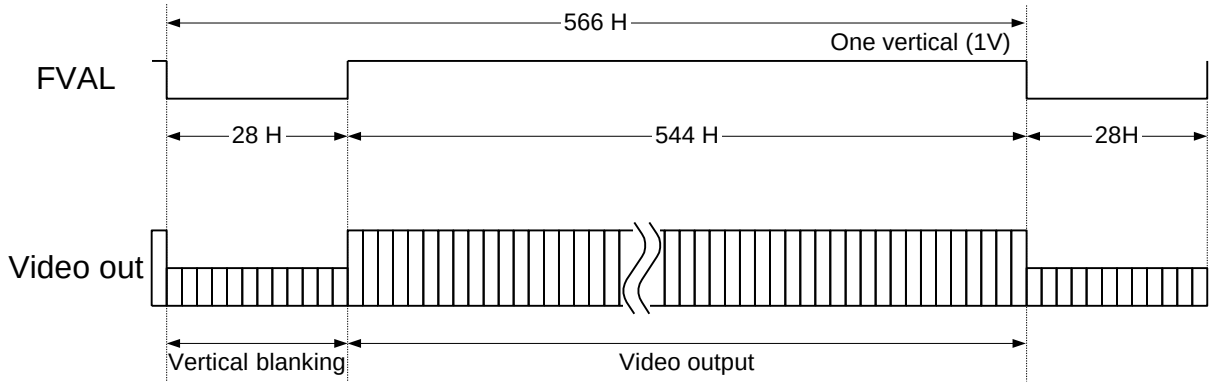


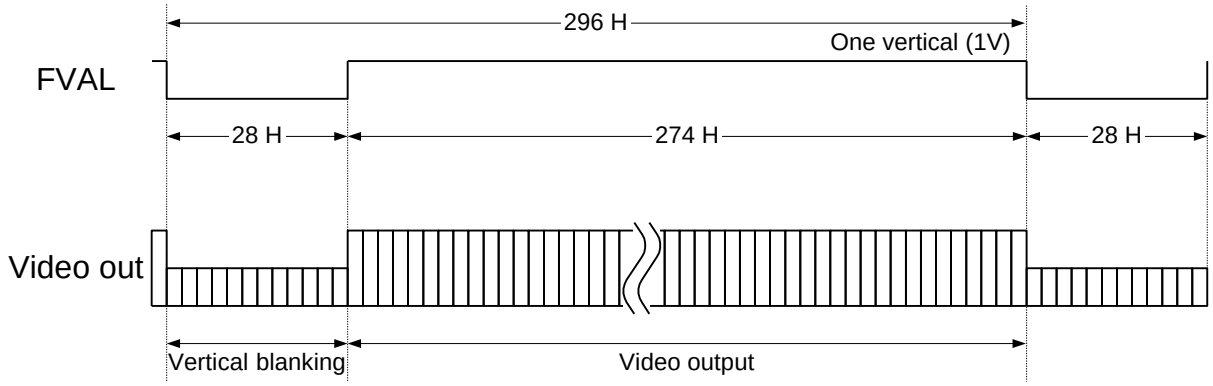
Table of Video Output on Binning Mode (STC-CMB2MCL / CMC2MCL)

Mode (29H)	Tap Number	Binning	Configuration	CameraLink Output PixelClock Frequency(MHz)	Horizontal Pixel (Pixel)	Sensor Output Pixel Clock(MHz)	FPS	Camera Link Output Bit
8	2	2 x 2	Base	85.0	1024	21.250	147.6	8/10/12
10	2	4 x 4	Base	85.0	512	42.500	295.2	8/10/12
9	4	2 x 2	Medium	85.0	1024	42.500	295.2	8/10/12
11	1	8 x 8	Base	85.0	256	42.500	295.2	8/10/12
8	2	2 x 2	Base	42.5	1024	10.625	73.8	8/10/12
10	2	4 x 4	Base	42.5	512	21.250	147.6	8/10/12
9	4	2 x 2	Medium	42.5	1024	21.250	147.6	8/10/12
11	1	8 x 8	Base	42.5	256	21.250	147.6	8/10/12
8	2	2 x 2	Base	85.0	1024	21.250	147.6	8/10/12

6. 2 x 2 Subsampling (STC-CMB2MCL / CMC2MCL)



7. 4 x 4 Subsampling (STC-CMB2MCL / CMC2MCL)



8. 8 x 8 Subsampling (STC-CMB2MCL / CMC2MCL)

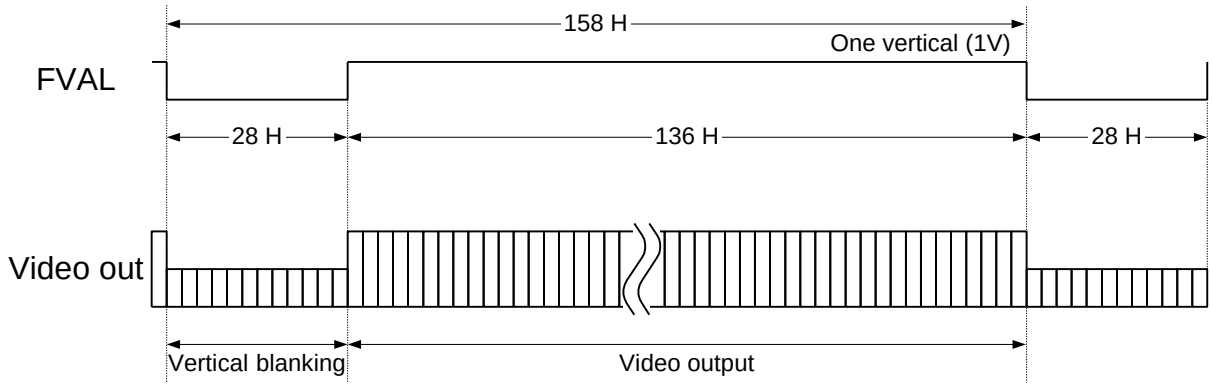
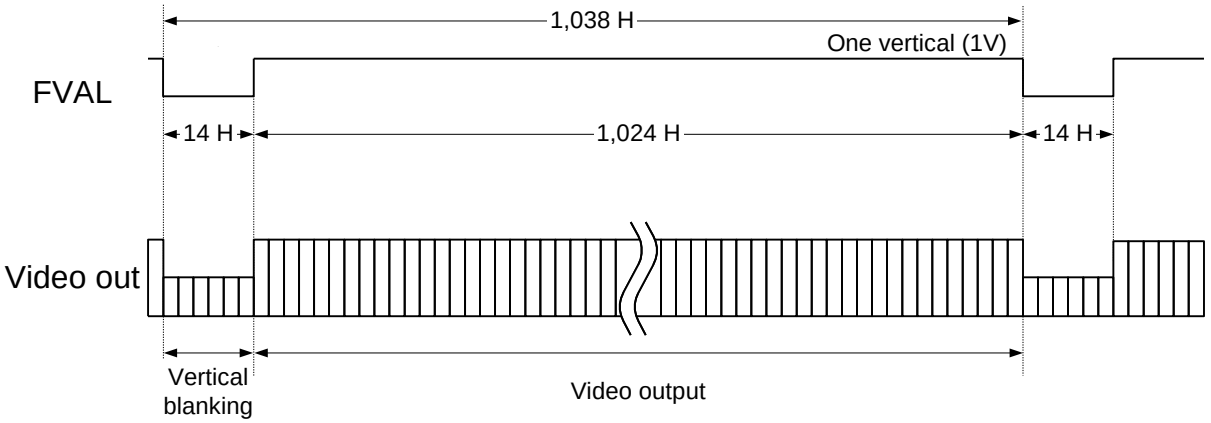


Table of Video Output on Subsampling Mode (STC-CMB2MCL / CMC2MCL)

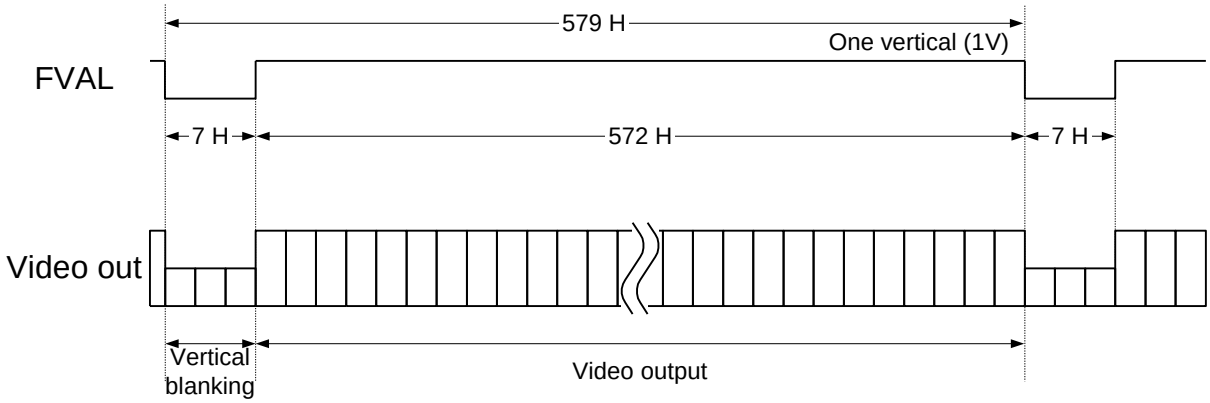
Subsampling reduces the output resolution and increases the frame rate without FOV (Field of View). This camera has row and column subsampling modes, this mode cannot reduce noise level.

Mode (29H)	Tap Number	Binning	Configuration	CameraLink Output PixelClock Frequency(MHz)	Horizontal Pixel (Pixel)	Sensor Output Pixel Clock(MHz)	FPS	Camera Link Output Bit
12	2	2 x 2	Base	85.0	1024	21.250	288.0	8/10
14	2	4 x 4	Base	85.0	512	42.500	1098.2	8/10
13	4	2 x 2	Medium	85.0	1024	42.500	576.0	8/10
15	1	8 x 8	Base	85.0	256	42.500	2008.9	8/10
12	2	2 x 2	Base	42.5	1024	10.625	144.0	8/10
14	2	4 x 4	Base	42.5	512	21.250	549.1	8/10
13	4	2 x 2	Medium	42.5	1024	21.250	288.0	8/10
15	1	8 x 8	Base	42.5	256	21.250	1004.4	8/10

9. 2 x 2 Binning (STC-CMB4MCL / STC-CMC4MCL)



10 .4 x 4 Binning (STC-CMB4MCL / CMC4MCL)



11. 8 x 8 Binning (STC-CMB4MCL / CMC4MCL)

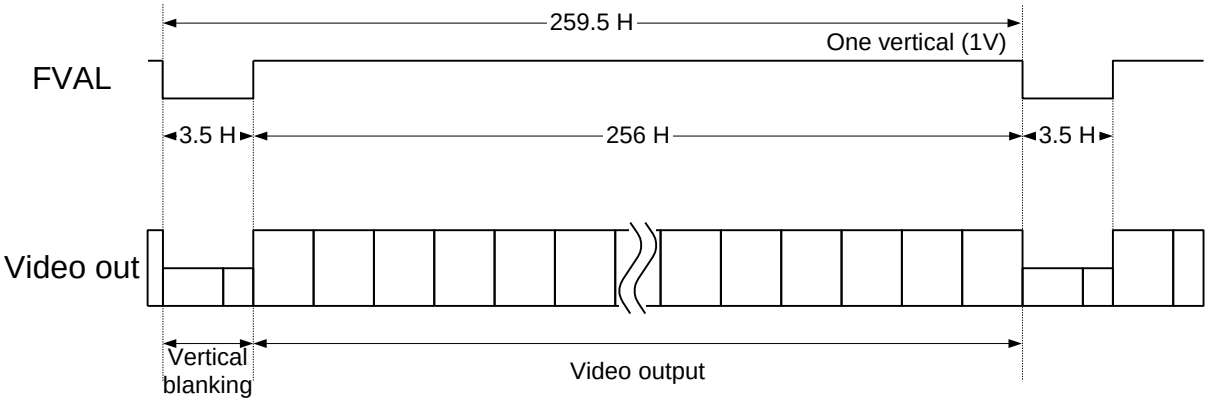
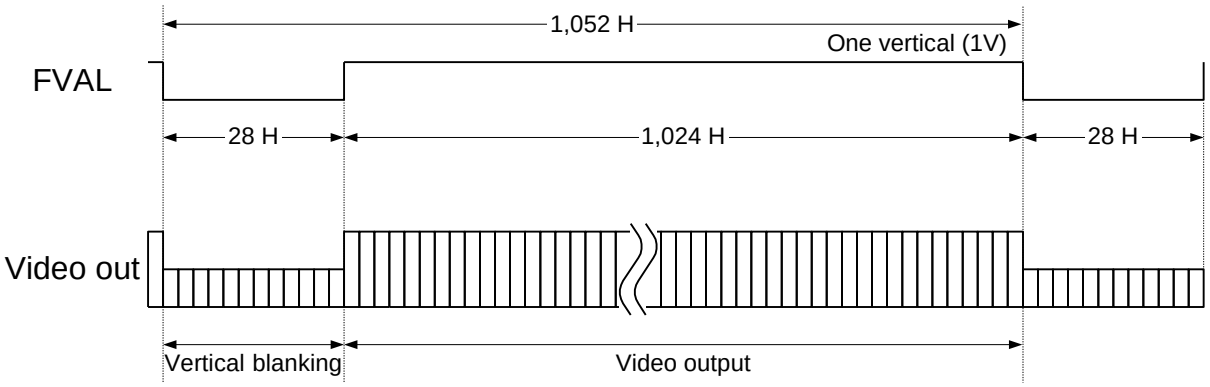


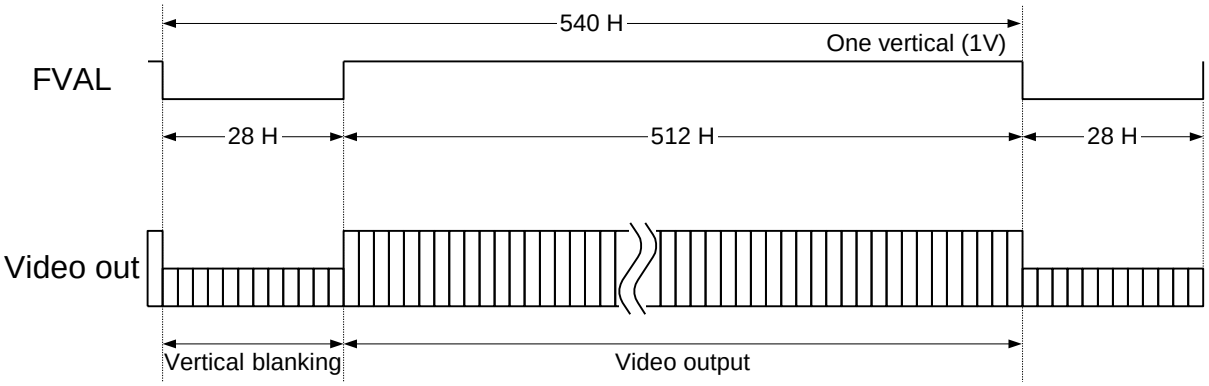
Table of Video Output on Binning Mode (STC-CMB4MCL / CMC4MCL)

Mode (29H)	Tap Number	Binning	Configuration	CameraLink Output PixelClock Frequency(MHz)	Horizontal Pixel (Pixel)	Sensor Output Pixel Clock(MHz)	FPS	Camera Link Output Bit
8	2	2 x 2	Base	85.0	1024	21.250	79.3	8/10/12
10	2	4 x 4	Base	85.0	512	42.500	158.7	8/10/12
9	4	2 x 2	Medium	85.0	1024	42.500	158.7	8/10/12
11	1	8 x 8	Base	85.0	256	42.500	158.7	8/10/12
8	2	2 x 2	Base	42.5	1024	10.625	39.7	8/10/12
10	2	4 x 4	Base	42.5	512	21.250	79.3	8/10/12
9	4	2 x 2	Medium	42.5	1024	21.250	79.3	8/10/12
11	1	8 x 8	Base	42.5	256	21.250	79.3	8/10/12

12. 2 x 2 Subsampling (STC-CMB4MCL / CMC4MCL)



13. 4 x 4 Subsampling (STC-CMB4MCL / CMC4MCL)



14. 8 x 8 Subsampling (STC-CMB4MCL / CMC4MCL)

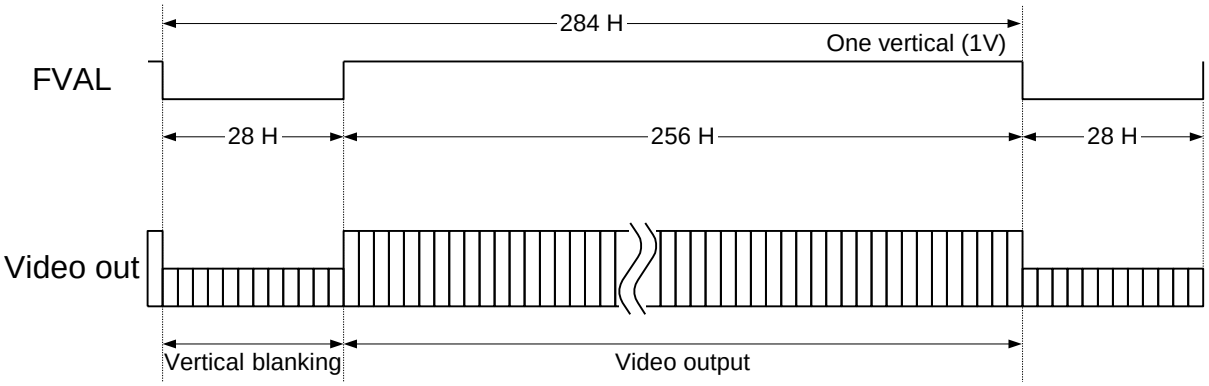
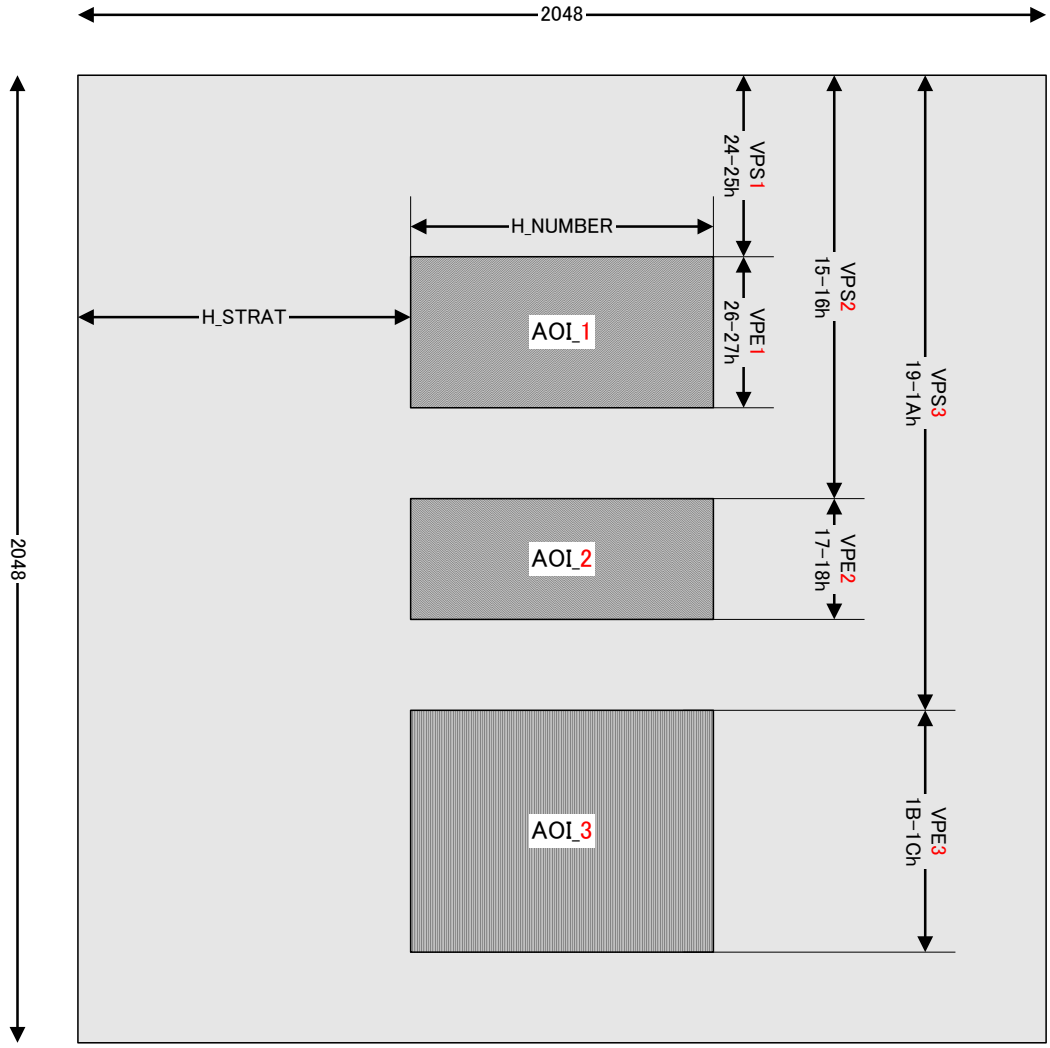


Table of Video Output on Subsampling mode (STC-CMB4MCL / CMC4MCL)

Mode (29H)	Tap Number	Binning	Configuration	CameraLink Output PixelClock Frequency(MHz)	Horizontal Pixel (Pixel)	Sensor Output Pixel Clock(MHz)	FPS	Camera Link Output Bit
12	2	2 x 2	Base	85.0	1024	21.250	156.6	8/10
14	2	4 x 4	Base	85.0	512	42.500	610.1	8/10
13	4	2 x 2	Medium	85.0	1024	42.500	313.2	8/10
15	1	8 x 8	Base	85.0	256	42.500	1160.1	8/10
12	2	2 x 2	Base	42.5	1024	10.625	78.3	8/10
14	2	4 x 4	Base	42.5	512	21.250	305.1	8/10
13	4	2 x 2	Medium	42.5	1024	21.250	156.6	8/10
15	1	8 x 8	Base	42.5	256	21.250	580.0	8/10

C. AOI Timing

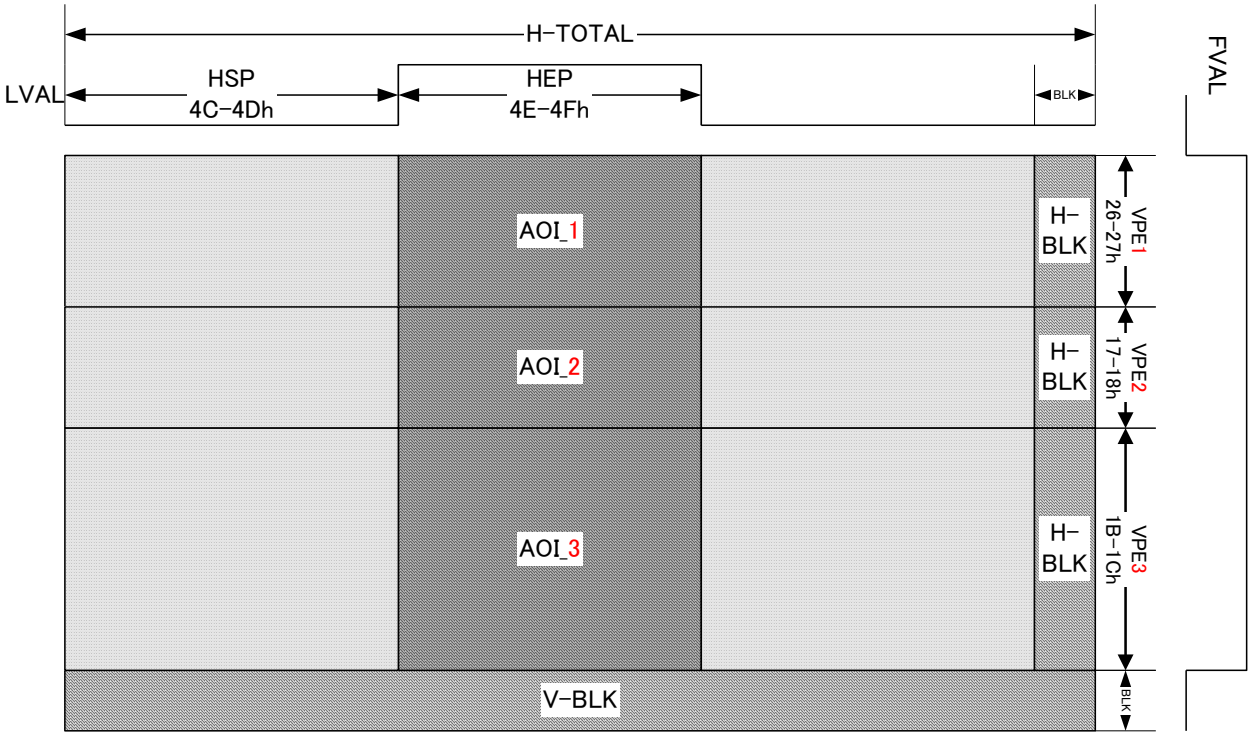
- This camera can set up to 8 AOI.
- AOI can be set with the Binning and Subsampling at the same time.
- The setting of the horizontal effective pixel and the horizontal effective pixels of changeable DVAL are different from each Camera Link output TAP number.
- The horizontal AOI can be controlled by LVAL and DVAL. The Video signal outputs directly, therefore the frame cannot increase in this mode.

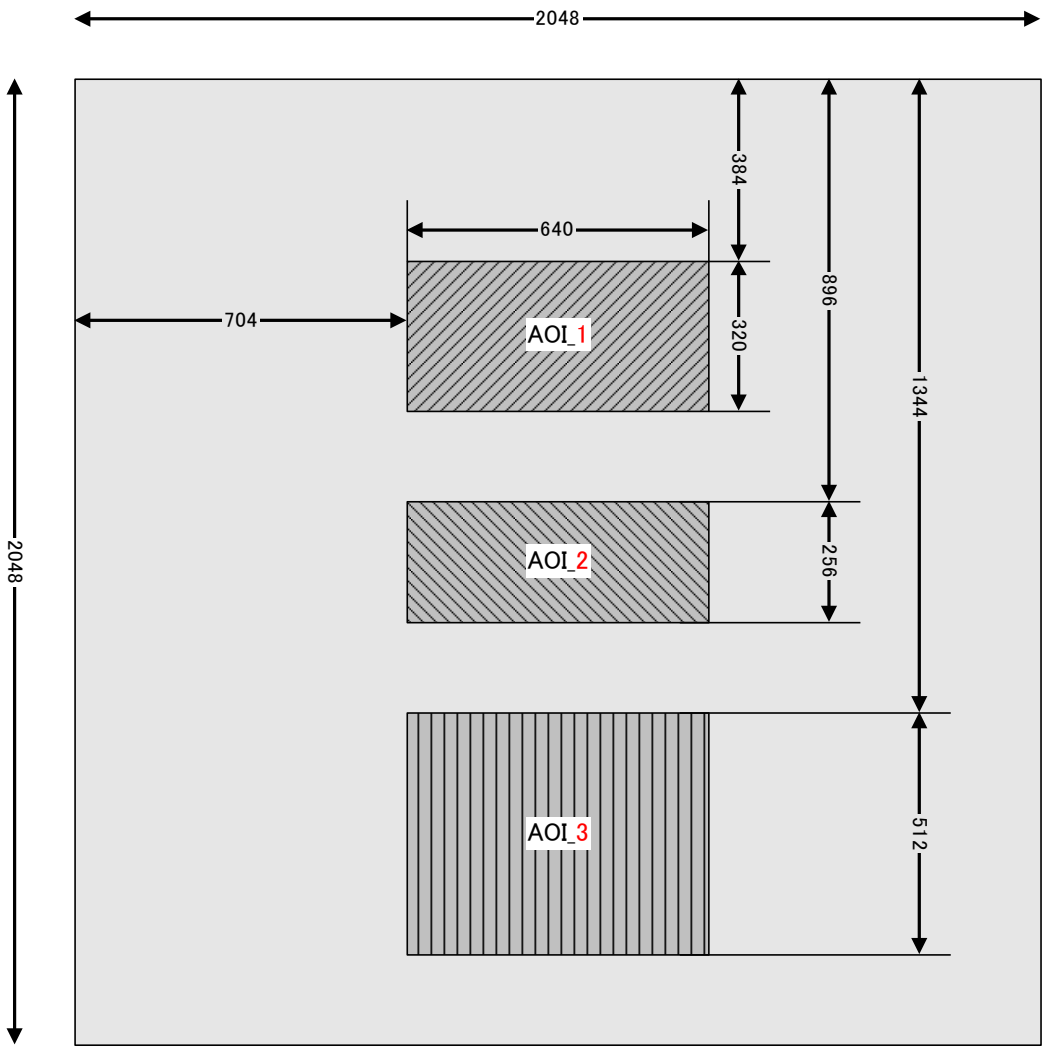


The Horizontal Effective Pixel
Register[4CH,4DH2-0]＝H_STRAT: Horizontal start position ÷ Output TAP number ÷ Binning or Subsampling number

The Horizontal Effective Pixels of Changeable DVAL
Register[4EH,4FH2-0]＝H_NUMBER: Horizontal available pixel number ÷ Output TAP number ÷ Binning or Subsampling number

When using the 10 TAP, horizontal pixel number is 2040 pixel using H_STRAT: Horizontal start position -4.
Register[4EH,4FH2-0]＝(H_NUMBER: Horizontal available pixel number -4)÷ Output TAP number ÷ Binning or Subsampling number


H-TOTAL
LVAL
HSP
4C-4Dh
HEP
4E-4Fh
BLK
FVAL
AOI_1
AOI_2
AOI_3
H-BLK
H-BLK
H-BLK
VPE1
26-27h
VPE2
17-18h
VPE3
1B-1Ch
V-BLK
BLK



e.g.

H_STRAT: Horizontal start position = 704
H_NUMBER: Horizontal available pixel number = 640
On 8TAP

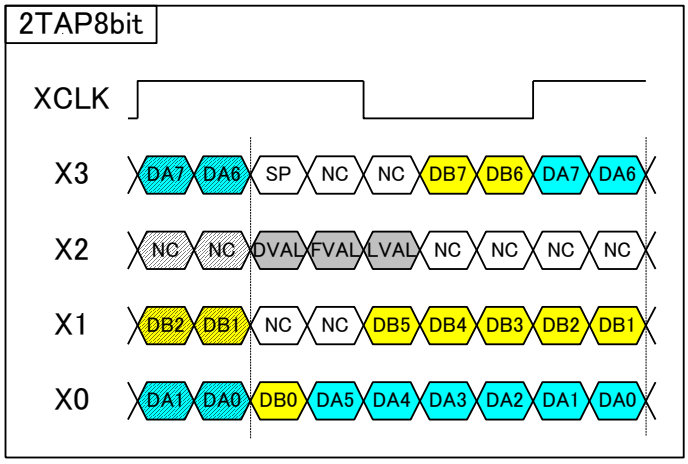
The horizontal effective pixel

Register[4CH,4DH2-0] ＝H_STRAT: Horizontal start position ÷ Output TAP number ÷ Binning or Subsampling number
＝ 704 ÷ 8 ÷ 1 = 88

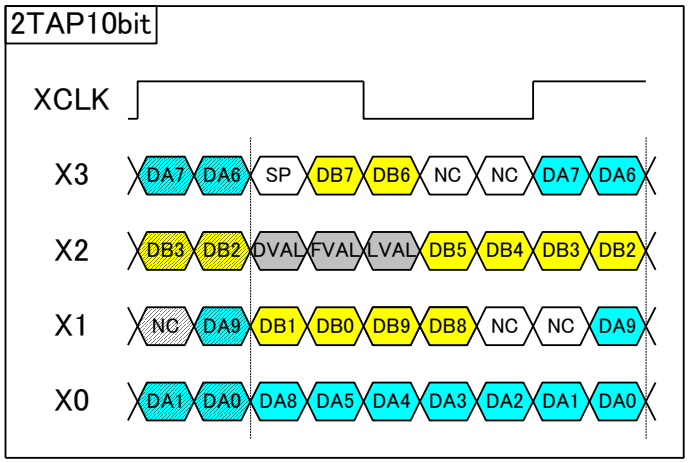
The horizontal effective pixels of changeable DVAL

Register[4EH,4FH2-0] ＝H_NUMBER: Horizontal available pixel number ÷ Output TAP number ÷ Binning or Subsampling number
＝ 640 ÷ 8 ÷ 1 = 80

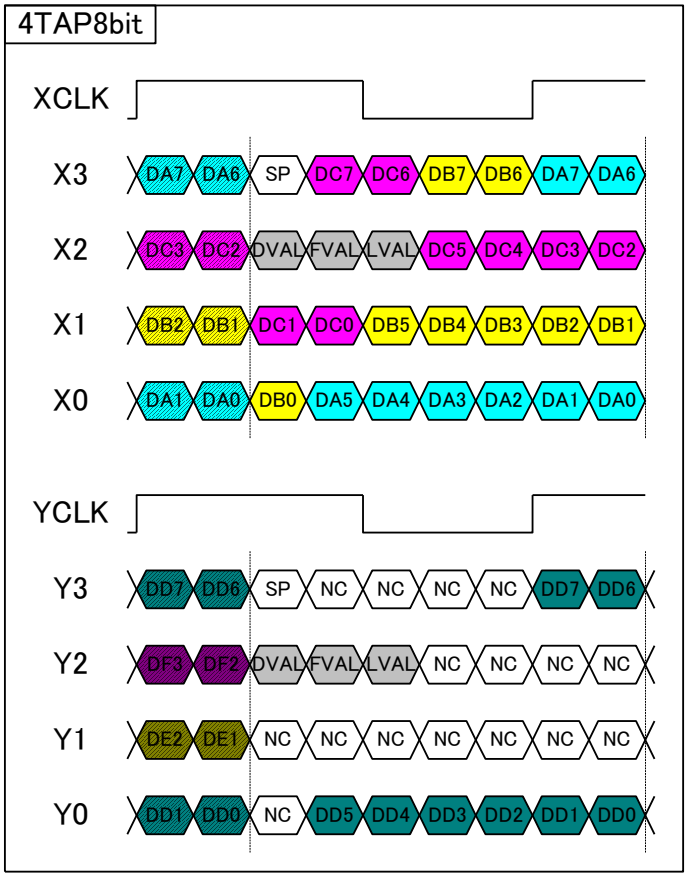
D. Camera Link Bit Assignment

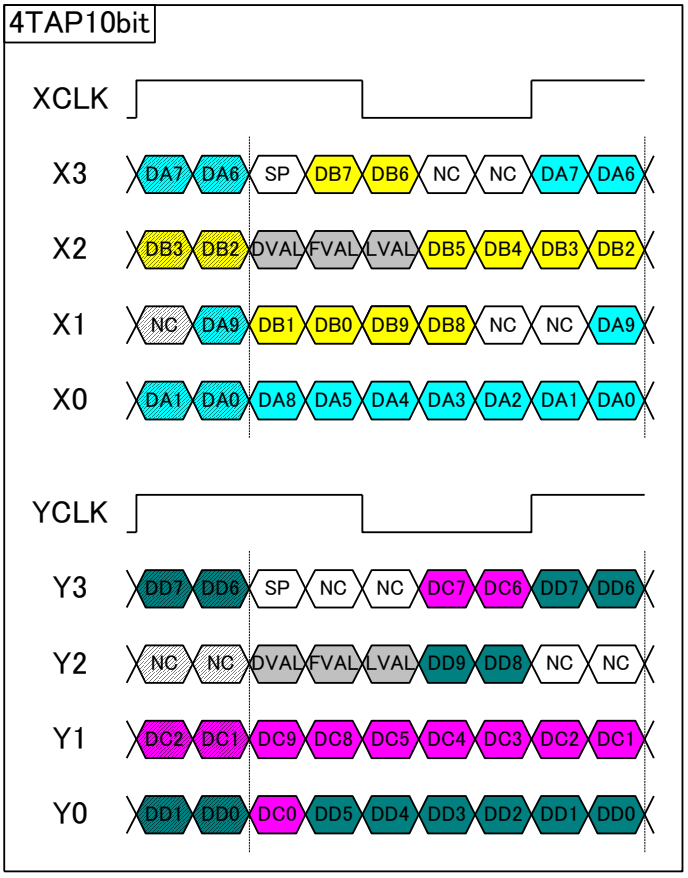


DA0 to DA7: 8 bit data for one pixel from TAP1.
DB0 to DB7: 8 bit data for one pixel from TAP2



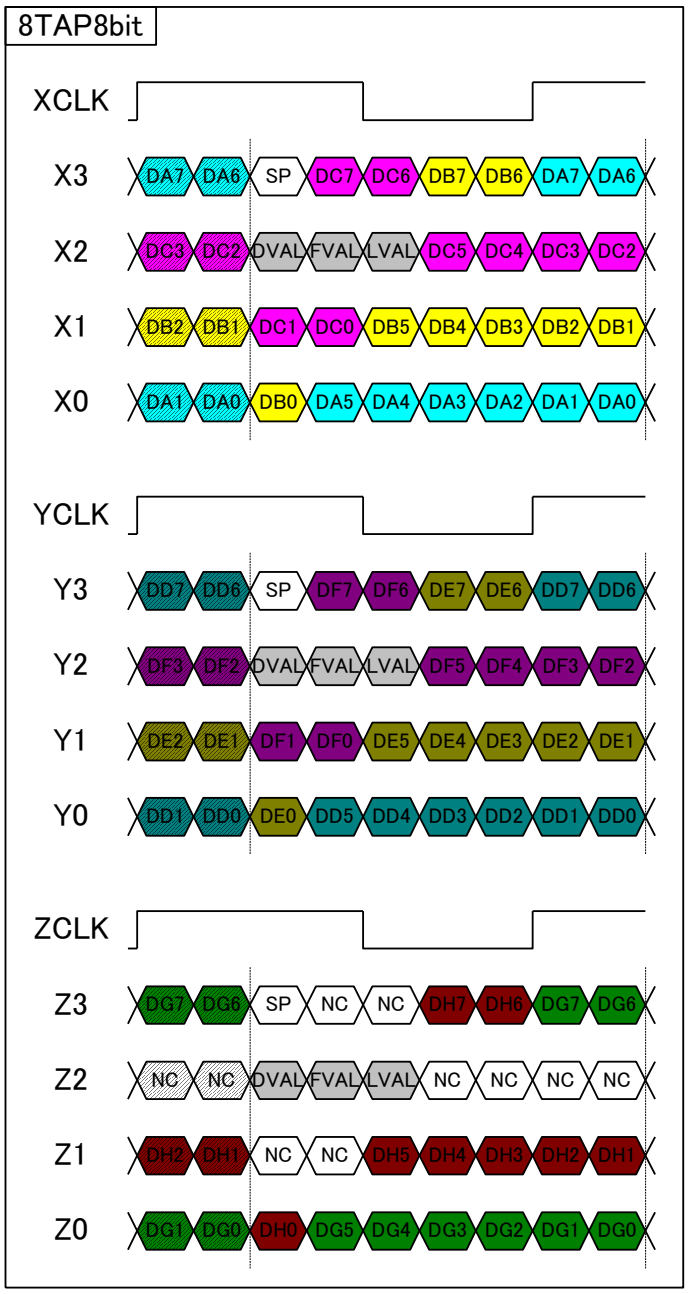
DA0 to DA9: 10 bit data for one pixel from TAP1
DB0 to DB9: 10 bit data for one pixel from TAP2



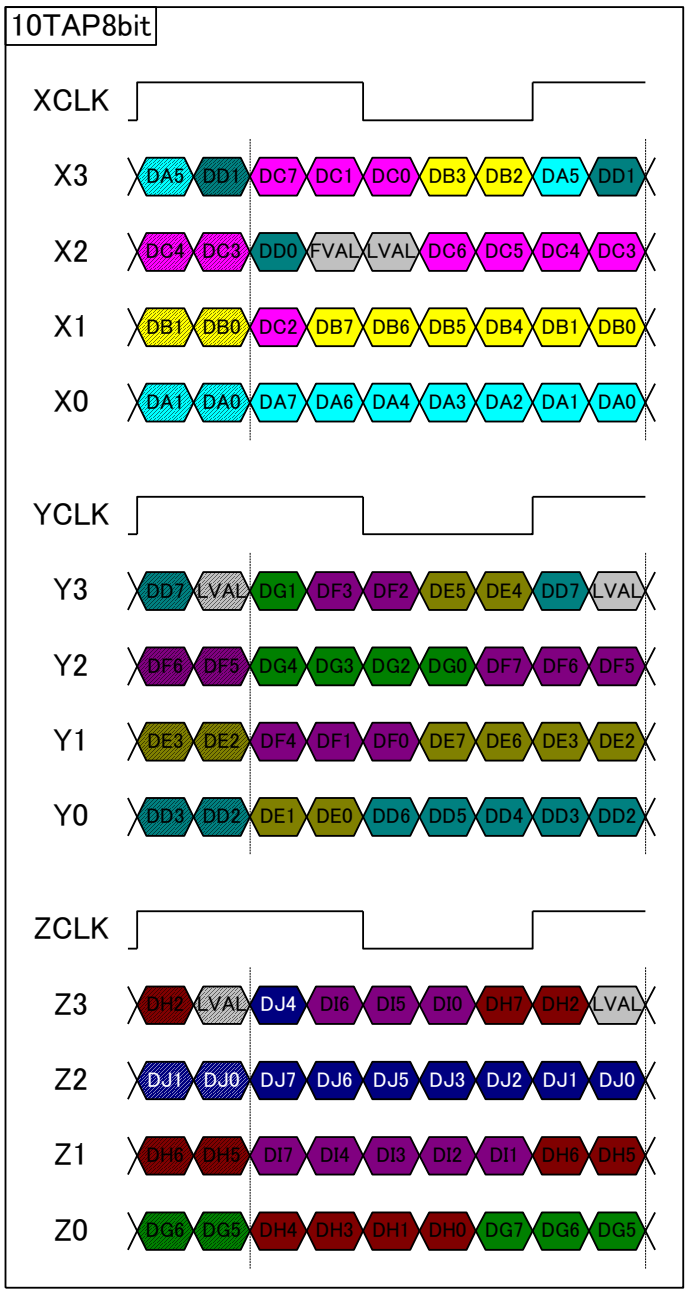


DA0 to DA7: 8 bit data for one pixel from TAP1
DB0 to DB7: 8bit data for one pixel from TAP2
DC0 to DC7: 8bit data for one pixel from TAP3
DD0 to DD7: 8bit data for one pixel from TAP4

DA0 to DA9:10bit data for one pixel from TAP1
DB0 to DB9:10bit data for one pixel from TAP2
DC0 to DC9:10bit data for one pixel from TAP3
DD0 to DD9:10bit data for one pixel from TAP4



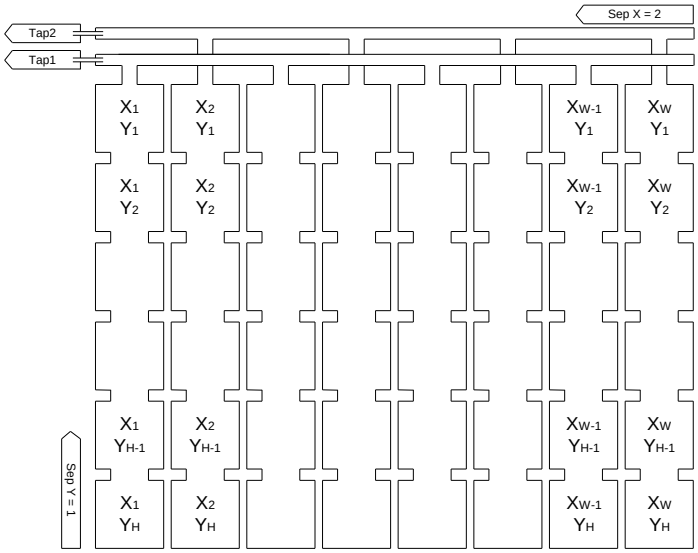
DA0 to DA7: 8 bit data for one pixel from TAP1
DB0 to DB7: 8 bit data for one pixel from TAP2
DC0 to DC7: 8 bit data for one pixel from TAP3
DD0 to DD7: 8 bit data for one pixel from TAP4
DE0 to DE7: 8 bit data for one pixel from TAP5
DF0 to DF7: 8 bit data for one pixel from TAP6
DG0 to DG7: 8 bit data for one pixel from TAP7
DH0 to DH7: 8 bit data for one pixel from TAP8



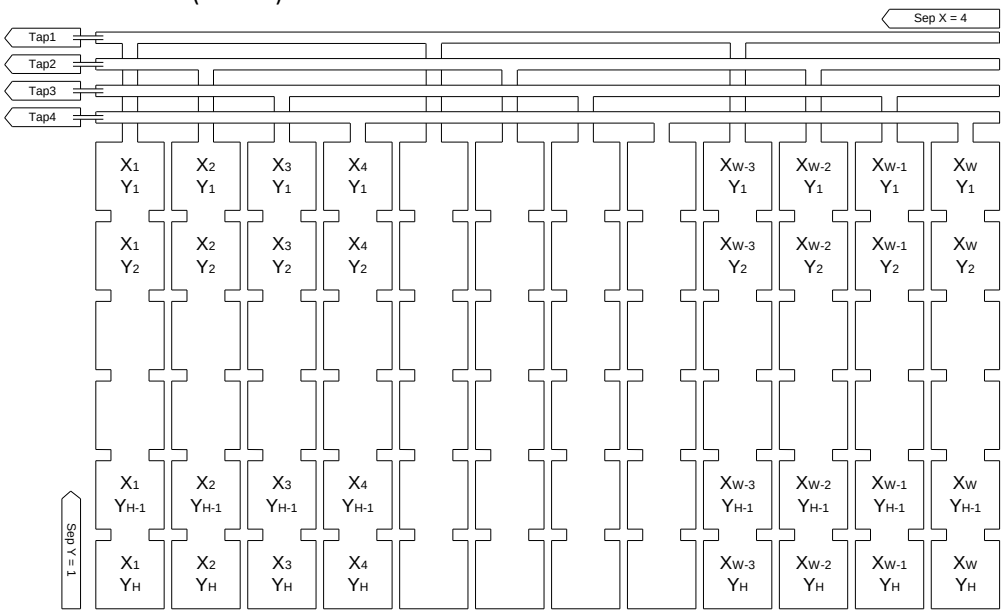
DA0 to DA7: 8 bit data for one pixel from TAP1
DB0 to DB7: 8 bit data for one pixel from TAP2
DC0 to DC7: 8 bit data for one pixel from TAP3
DD0 to DD7: 8 bit data for one pixel from TAP4
DE0 to DE7: 8 bit data for one pixel from TAP5
DF0 to DF7: 8 bit data for one pixel from TAP6
DG0 to DG7: 8 bit data for one pixel from TAP7
DH0 to DH7: 8 bit data for one pixel from TAP8
DI0 to DI7: 8 bit data for one pixel from TAP9
DJ0 to DJ7: 8 bit data for one pixel from TAP10

E. Camera Link TAP Geometry

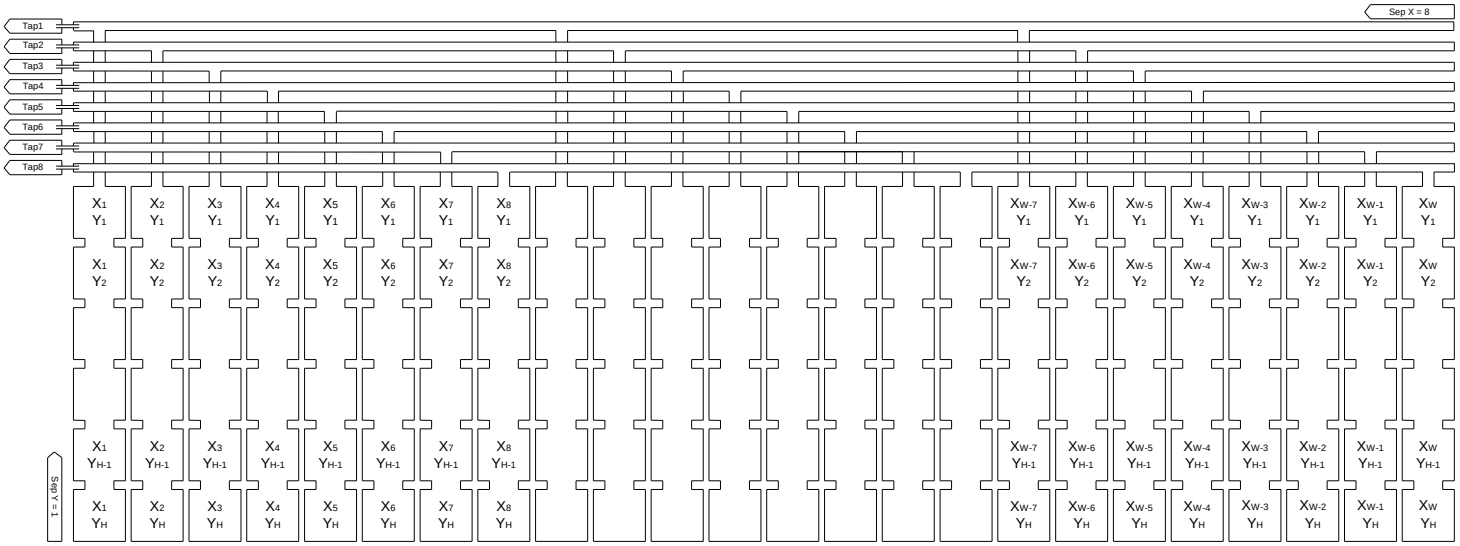
1. 2TAP (1X2-1Y)



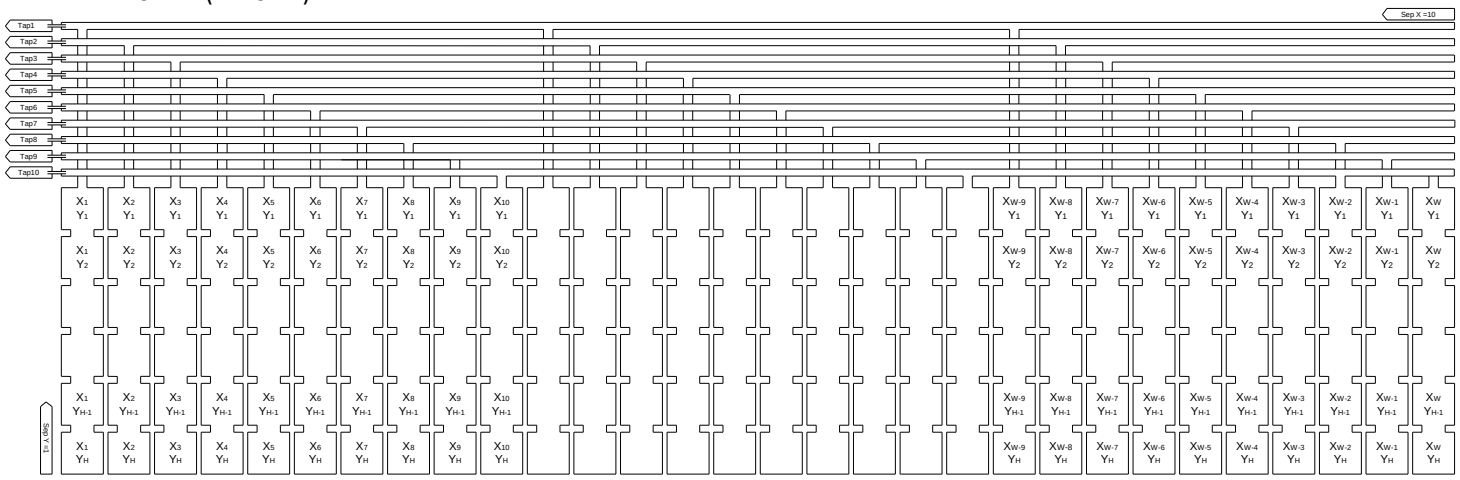
2. 4 TAP (1X4-1Y)



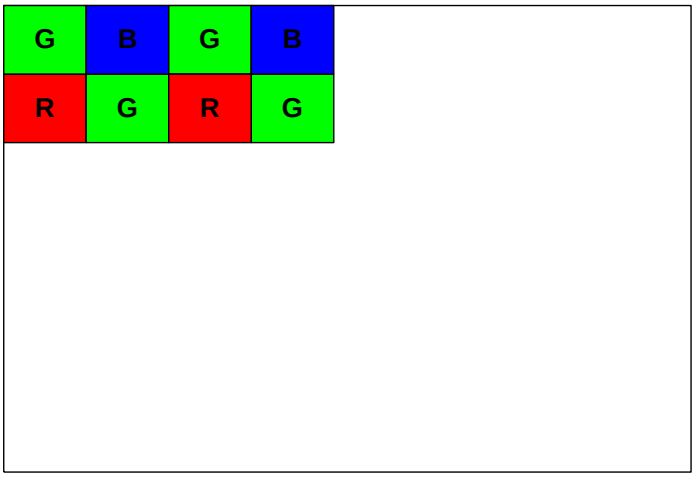
3. 8 TAP (1X8-1Y)



4. 10 TAP (1X10-1Y)



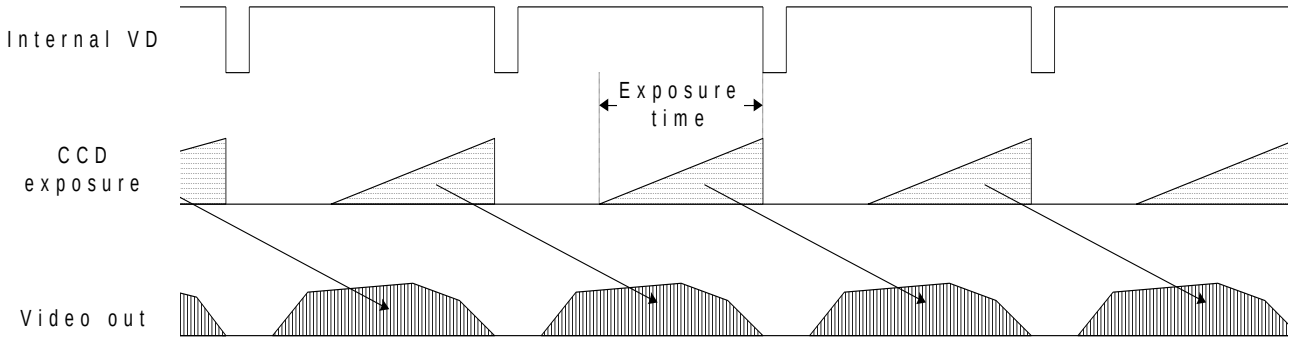
F. Bayer Pattern for Color Model (Only STC-CMC2MCL / STC-CMC4MCL)



V. Camera Function Modes

A. Normal Mode

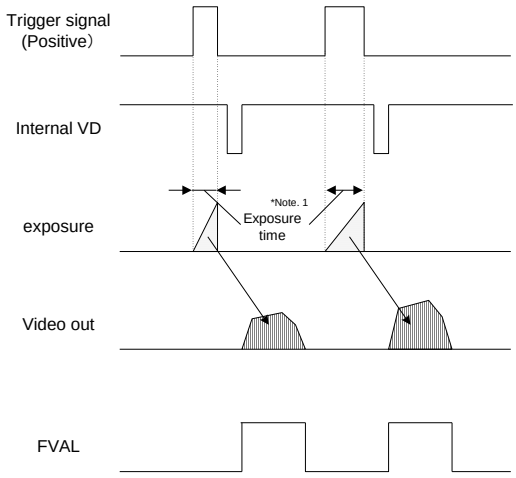
1. Normal Mode (Electronic Shutter)



B. Pulse Width Trigger Mode

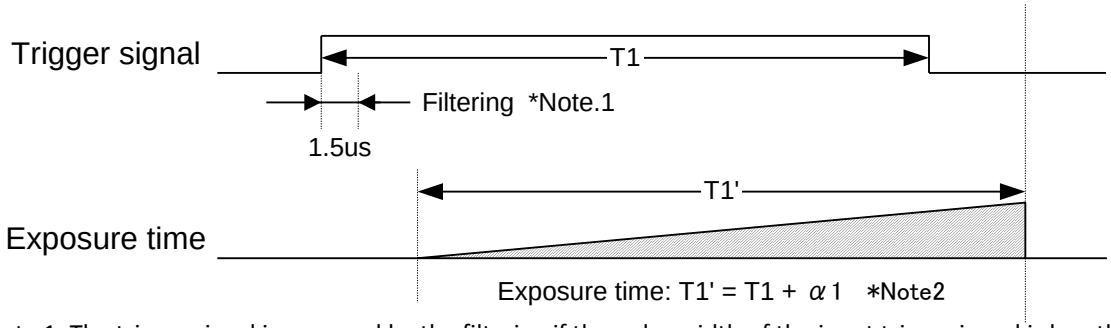
In this trigger mode with positive polarity, the camera exposure starts at the rising edge of the trigger pulse and stops at the falling edge of the trigger pulse. Therefore, in the case of the exposure positive polarity is selected, the exposure periods are the high states of the trigger pulse.

1. Pulse Width Trigger Mode (V-Reset)



Note 1: The exposure time is set by the pulse width of the trigger signal.

2. Pulse Width Trigger Mode (Exposure Timing)



Note 1: The trigger signal is removed by the filtering if the pulse width of the input trigger isngal is less than 15us. Please input the trigger signal that has more than 1.5 us pulse width.

Note 2: α 1(Exposure time offset) is different from using clock, TAP number.

The formula of α 1 Exposure time offset (STC-CMB2MCL / CMC2MCL , STC-CMB4MCL / CMC4MCL)

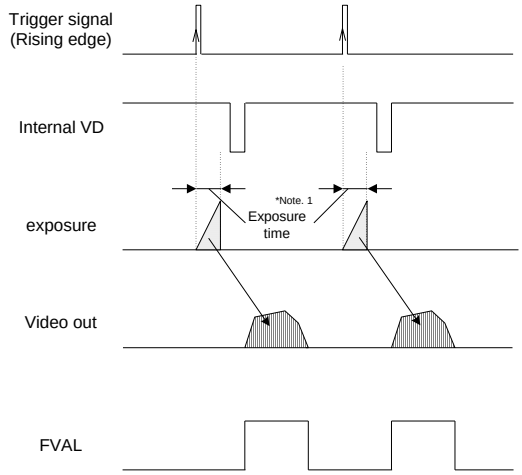
α 1 = 129 / Sensor Output Pick Clock x 20 x 0.43

Mode (29H)	TAP Number	Scan	Configuration	CameraLink Output PixelClock Frequency 85MHz		CameraLink Output PixelClock Frequency 42.5MHz	
				Sensor Output Pixel Clock(MHz)	**α 1** (us)	**Sensor Output Pixel Clock(MHz)**	**α 1** (us)
0	2	Progressive	Base	10.625	104.4	5.3125	208.8
1	4	Progressive	Medium	21.250	52.2	10.625	104.4
2	8	Progressive	Full	42.500	26.1	21.250	52.2
3	10	Progressive		48.000	23.1	24.000	46.2
4							
5	2	Progressive	Base	21.250	52.2	10.625	104.4
6	2	Progressive	Base	42.500	26.1	21.250	52.2
7	4	Progressive	Medium	42.500	26.1	21.250	52.2
8	2	Binning	Base	21.250	52.2	10.625	104.4
9	4	Binning	Medium	42.500	26.1	21.250	52.2
10	2	Binning	Base	42.500	26.1	21.250	52.2
11	1	Binning	Base	42.500	26.1	21.250	52.2
12	2	Sub Sampling	Base	21.250	52.2	10.625	104.4
13	4	Sub Sampling	Medium	42.500	26.1	21.250	52.2
14	2	Sub Sampling	Base	42.500	26.1	21.250	52.2
15	1	Sub Sampling	Base	42.500	26.1	21.250	52.2

C. Edge Preset Trigger Mode

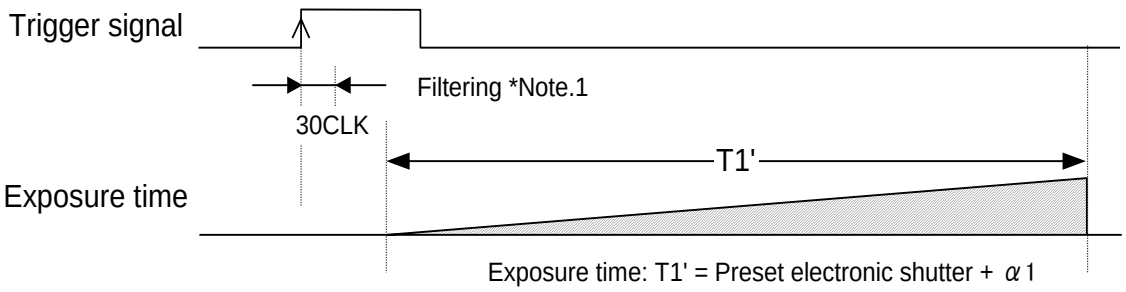
In this trigger mode, the camera exposure starts at the rising edge of the trigger pulse. Exposure duration time is preset by the DIP Switch settings.

1. Edge Preset Trigger Mode (V-Reset)



Note 1: The exposure time is set by the preset electronic shutter speed.

2. Edge Preset Trigger Mode (Exposure Timing)



Note 1: The trigger signal is removed by the filtering if the pulse width of the input trigger signal is less than 15us. Please input the trigger signal that has more than 1.5us pulse width.

Note 2: α 1(Exposure time offset) is different from using clock, TAP number.

The formula of α 1 Exposure time offset (STC-CMB2MCL / CMC2MCL, STC-CMB4MCL / CMC4MCL)

α 1 = 129 / Sensor Output Pixel Clock x 20 x 0.43

Mode (29H)	TAP Number	Scan	Configuration	CameraLink Output PixelClock Frequency 85MHz		CameraLink Output PixelClock Frequency 42.5MHz	
				Sensor Output Pixel Clock(MHz)	**α 1** (us)	**Sensor Output Pixel Clock(MHz)**	**α 1** (us)
0	2	Progressive	Base	10.625	104.4	5.3125	208.8
1	4	Progressive	Medium	21.250	52.2	10.625	104.4
2	8	Progressive	Full	42.500	26.1	21.250	52.2
3	10	Progressive		48.000	23.1	24.000	46.2
4							
5	2	Progressive	Base	21.250	52.2	10.625	104.4
6	2	Progressive	Base	42.500	26.1	21.250	52.2
7	4	Progressive	Medium	42.500	26.1	21.250	52.2
8	2	Binning	Base	21.250	52.2	10.625	104.4
9	4	Binning	Medium	42.500	26.1	21.250	52.2
10	2	Binning	Base	42.500	26.1	21.250	52.2
11	1	Binning	Base	42.500	26.1	21.250	52.2
12	2	Sub Sampling	Base	21.250	52.2	10.625	104.4
13	4	Sub Sampling	Medium	42.500	26.1	21.250	52.2
14	2	Sub Sampling	Base	42.500	26.1	21.250	52.2
15	1	Sub Sampling	Base	42.500	26.1	21.250	52.2

VI. The Communication Protocol Specifications

This camera has the communication function that enables external devices like PC controls the camera functions. Please use "ClCtrl" communication software or use following the communication protocol to communication to the camera.

Note:

Communication problems may occur under the following conditions:

1. When the external sync frequency is illegal (more than 1% off from the specified frequency)
2. When external sync is unstable (Bad external sync signal)
3. For one second after switching from / to external sync mode or to / from internal sync mode.
4. For one second after switching the frame rate.

A. The Communication Method

UART (RS232C), Binary Communication.

B. The Communication Settings

	Settings
Baud rate	9,600bps / 38,400bps /57,600bps / 115,200bps
Data bit	8bit
Parity	None
Stop bit	1bit
Flow control	None

C. The Communication Format

1. The format for sending data from the PC to the camera is as follows:

a. Sending the Read Command

SOF (8bit)	Device code (6bit)	Read (1bit)	Page selection (1bit)	Command code (8bit)	Data length (8bit)	Data (1byte)	EOF (8bit)

b. Sending the Write Command

SOF (8bit)	Device code (6bit)	Write (1bit)	Page selection (1bit)	Command code (8bit)	Data length (8bit)	Data (Data length byte)	EOF (8bit)

2. The format for receiving data from the camera is as follows:

a. After the read command has been sent

SOF (8bit)	Data length (8bit)	Data (Data length byte)	EOF (8bit)

b. After the write command has been sent

SOF (8bit)	Data length (8bit) "00"	Receiving code (8bit)	EOF (8bit)

3. Descriptions of the Format

Name	Descriptions
SOF	**S**tart **o**f the **f**rame Sets (or gets) the value is as “02H” always.
Device code	Sets the device code of the camera is as “000000”.
Read / Write	Sets (or gets) “0” when send read command. Sets (or gets) “1” when send write command.
Page selection	Sets “0” when access to the command register of the camera Gets current data from the command register when sent read command. The data of the command register is replaced by the sent data when sent write command. **The data of the EEPROM is not replaced.** Sets “1” when access to the EEPROM of the camera The camera works with the data of the EEPROM when the power on the camera. Gets the data from the EEPROM when sent read. The data of the EEPROM is replaced by sent data when sent write command. The camera sends the receiving coce as "01H" to the PC after the data of the EEPROM is replaced. The camera rejects other commands while the data of the EEPROM is being replaced (approximately 5 msec. / byte).
Command code	**Please refer from the following page.**
Data length	Data length (Unit: byte) Receiving data The data length is depending on the command after sent read command. The data length is “00H” after sent write command. Sending data The data length is 1 byte when send read command. The data length is depending on the command when send write command.
Data	The value of the data is depending on the command
EOF	**E**nd **o**f the **f**rame Sets (or gets) the value is as “03H” always
Receiving code	Result of the sending command

4. Example command

Send the read command to read the 00H address data of the register

02, 00, 00, 01, 00, 03

SOF, (Device code/Read/Register), Command code, Data length, Data, EOF

The return command

02, 01, 00, 03

D. The Camera Control Commands

1. The Camera Commands List

Note1: The data unit of each command is 1byte (8bit).

Note2: The data can be saved to the "EEPROM" if an "x" is in the "Save to EEPROM" column in the list.

Note3: The camera is operating with the data of the EEPROM when the camera is powered on.

Command No.	Read / Write	Save to EEPROM	Function	Initial data	Data range
00 to 0FH			Reserved	-	-
10H	Read / Write	x	The camera function mode 1 (8bit: D[7..0])	0	
11H	Read / Write	x	The camera function mode 2 (8bit: D[7..0])	08H	
12H	Read / Write	x	The camera function mode 3 (8bit: D[7..0])	50H	
13H			Reserved	-	-
14H	Read / Write	x	The communication mode (8bit: D[7..0])	1	
15H	Read / Write	x	The start line of the variable partial scanning B (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
16H	Read / Write	x	The start line of the variable partial scanning B (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
17H	Read / Write	x	The effective lines of the variable partial scanning B (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,088
18H	Read / Write	x	The effective lines of the variable partial scanning B (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,048
19H	Read / Write	x	The start line of the variable partial scanning C (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
1AH	Read / Write	x	The start line of the variable partial scanning C (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
1BH	Read / Write	x	The effective lines of the variable partial scanning C (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,088
1CH	Read / Write	x	The effective lines of the variable partial scanning C (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,048
1D to1FH			Reserved	-	-
20H	Read / Write	x	The exposure time of the electronic shutter (16bit: D[7..0])	CMB/CMC2MCL: 1088	0 to 16,777,215
21H	Read / Write	x	The exposure time of the electronic shutter (16bit: D[15..8])	CMB/CMC4MCL: 2048	
22H	Read / Write	x	The exposure time of the electronic shutter (16bit: D[23..16])		
23H			Reserved	-	-
24H	Read / Write	x	The start line of the variable partial scanning (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
25H	Read / Write	x	The start line of the variable partial scanning (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
26H	Read / Write	x	The effective lines of the variable partial scanning (16bit: D[7..0])	CMB/CMC2MCL: 1,088	CMB/CMC2MCL: 0 to 1,088
27H	Read / Write	x	The effective lines of the variable partial scanning (16bit: D[15..8])	CMB/CMC4MCL: 2,048	CMB/CMC4MCL: 0 to 2,048
28H	Read / Write	x	The delay time for the trigger (8bit: D[7..0])	0	0 to 255
29H	Read / Write	x	The camera function mode (8bit: D[7..0])	2	
2A - 2FH			Reserved	-	-
30H	Read / Write	x	ADC gain (8bit: D[7..0])	44	0 to 63
31H	Read / Write	x	The digital gain (8bit: D[7..0])	The factory adjusted value	-
32 to 37H			Reserved	-	-
38H	Read / Write	x	The clamp level (8bit: D[7..0])	40	0 to 255
39H			Reserved	-	-
3AH	Read / Write	x	R gain (8bit: D[7..0])	0	0 to 255
3BH	Read / Write	x	B gain (8bit: D[7..0])	0	0 to 255
3CH	Read / Write	x	Gr gain (8bit: D[7..0])	0	0 to 255
3DH	Read / Write	x	Gb gain (8bit: D[7..0])	0	0 to 255
3E to 3FH			Reserved	-	-
40H	Read / Write	x	The start line of the variable partial scanning D (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
41H	Read / Write	x	The start line of the variable partial scanning D (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
42H	Read / Write	x	The effective lines of the variable partial scanning D (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,088
43H	Read / Write	x	The effective lines of the variable partial scanning D (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,048
44 to 46H			Reserved	-	-
47H	Read / Write	x	HDR slope (8bit: D[7..0])	0	
48 to 4AH			Reserved	-	-
4BH	Read / Write	x	PGA (8bit: D[7..0])	96	

Command No.	Read / Write	Save to EEPROM	Function	Initial data	Data range
4CH	Read / Write	x	The horizontal start position of changeable DVAL (16bit: D[7..0])		
4DH	Read / Write	x	The horizontal start position of changeable DVAL (16bit: D[15..8])		
4EH	Read / Write	x	The horizontal effective pixels of changeable DVAL (16bit: D[7..0])		
4FH	Read / Write	x	The horizontal effective pixels of changeable DVAL (16bit: D[15..8])		
50H	Read / Write	x	The start line of the variable partial scanning E (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
51H	Read / Write	x	The start line of the variable partial scanning E (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
52H	Read / Write	x	The effective lines of the variable partial scanning E (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,088
53H	Read / Write	x	The effective lines of the variable partial scanning E (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,048
54 to 55H			Reserved	-	-
56H	Read / Write	x	Knee1 parameter (8bit: D[7..0])		
57H	Read / Write	x	Knee2 parameter (8bit: D[7..0])		
58 to 5AH			Reserved	-	-
5BH	Read / Write	x	Vlow2 voltage (8bit: D[7..0])	96	0 to 255
5C to 5EH			Reserved	-	-
5FH	Read / Write	x	Vlow3 voltage (8bit: D[7..0])	96	0 to 255
60 to 67H			Reserved	-	-
68H	Read / Write	x	Reverse mode (8bit: D[7..0])	0	
69 to 77H			Reserved	-	-
78H	Read / Write	x	Test pattern selection (8bit: D[7..0])	0	
79H	Read / Write	x	Image effect selection (8bit: D[7..0])	0	
7A to 8FH			Reserved	-	-
90H	Read / Write	x	The start line of the variable partial scanning F (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
91H	Read / Write	x	The start line of the variable partial scanning F (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
92H	Read / Write	x	The effective lines of the variable partial scanning F (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,088
93H	Read / Write	x	The effective lines of the variable partial scanning F (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,048
94H	Read / Write	x	The start line of the variable partial scanning G (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
95H	Read / Write	x	The start line of the variable partial scanning G (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
96H	Read / Write	x	The effective lines of the variable partial scanning G (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,088
97H	Read / Write	x	The effective lines of the variable partial scanning G (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,048
98H	Read / Write	x	The start line of the variable partial scanning H (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,087
99H	Read / Write	x	The start line of the variable partial scanning H (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,047
9AH	Read / Write	x	The effective lines of the variable partial scanning H (16bit: D[7..0])	CMB/CMC2MCL: 0	CMB/CMC2MCL: 0 to 1,088
9BH	Read / Write	x	The effective lines of the variable partial scanning H (16bit: D[15..8])	CMB/CMC4MCL: 0	CMB/CMC4MCL: 0 to 2,048
9Cto D7H			Reserved	-	-
D8H	Read / Write	x	Pixel Defect	0	
D9H	Read / Write	x	LED control (8bit: D[7..0])	0	
DAto FFH			Reserved	-	-

2. Descriptions of the Camera Control Commands (The underlined settings are the factory default settings).

Command No.	Command descriptions
10H: MOD1[7..0]	[The camera function mode 1] Initial data: 0 Sets the camera function mode. D[7..0] D7: No function — Set always as "0" D6: Trigger polarity — 0: Positive, / 1: Negative D5: Trigger mode — 0: Edge preset, / 1: Pulse width D4: Binning mode — 0: OFF (Normal), / 1: ON (Binning) D3 to D0: No function — Sets always as "0000"
11H: MOD2[7..0]	[The camera function mode 2] Initial data: 08H Sets the camera function mode. D[7..0] D7 : No function — Sets always as "0" D6 to D5: CameraLink Clock — 00: 85MHz / 01: 42.5MHz 10: No function(Prohibited setting. Do not set these values) 11: No function(Prohibited setting. Do not set these values) D4: No function — Sets always as "0" D3: Function mode — 0: Trigger mode, / 1: Continuous mode D2 to D0: Partial scanning — Sets always as "000" No video output without the trigger signal input while the camera works with the trigger mode.
12H: MOD3[7..0]	[The camera function mode 3] Initial data: 40H Sets the camera function mode. D[7..0] | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | D7 to D6: Video out — 00: 10bit, / 01: 8bit, 10: 12bit *Note 12H, 11: No function (Prohibited setting. Do not set these values) D5: Trigger signal input connector 0: Camera Link (CC1), / 1: Power/IO connector (No. 2 pin, SP4) D4: Exposure start mode — 0: Normal, / 1: Horizontal Synchronization D3 to D0: No function — Sets always as "000"
14H: UART[7..0]	[The communication mode] Initial data: 1 Sets the communication mode. D[7..0] D7 to D2: No function — Sets always as "000000" D1 to D0: Communication mode 00: 38,400bps, / 01: 9,600bps 10: 57,600bps, / 11: 115,200bps

Command No.	Command descriptions
15H: PSRB[7..0] 16H: PSRB[15..8]	[The start line of the variable partial scanning B] STC-CMB2MCL/CMC2MCL: Initial data: PSRB[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSRB[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning B. Actual start line of the partial scanning B = this value + 1
17H: PWRB[7..0] 18H: PWRB[15..8]	[The effective lines of the variable partial scanning B] STC-CMB2MCL/CMC2MCL: Initial data: PWRB[15..0] = 0, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWRB[15..0] = 0, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning B. The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048
19H: PSRC[7..0] 1AH: PSRC[15..8]	[The start line of the variable partial scanning C] STC-CMB2MCL/CMC2MCL: Initial data: PSRC[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSRC[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning C. Actual start line of the partial scanning C = this value + 1
1BH: PWRC[7..0] 1CH: PWRC[15..8]	[The effective lines of the variable partial scanning C] STC-CMB2MCL/CMC2MCL: Initial data: PWRC[15..0] = 0, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWRC[15..0] = 0, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning C. The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048

Command No.	Command descriptions
20H: SVR[7..0] 21H: SVR[15..8] 22H: SVR[23..16]	[The exposure time of the electronic shutter] Initial data: SVR[23..0] = 0, data range: 0 to 16,777,215 Sets the preset shutter speed for electronic shutter. Exposure time (shutter speed) = SVR[23..0] x (129 pixels) x (1CLK cycle) STC-CMB2MCL/CMC2MCL When set 0 for SVR[23..0], the camera works with 1,088 line exposure. STC-CMB4MCL/CMC4MCL When set 0 for SVR[23..0], the camera works with 2,048 line exposure.
24H: PSR[7..0] 25H: PSR[15..8]	[The start line of the variable partial scanning] STC-CMB2MCL/CMC2MCL: Initial data: PSR[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSR[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning. Actual start line of the partial scanning = this value + 1
26H: PWR[7..0] 27H: PWR[15..8]	[The effective lines of the variable partial scanning] STC-CMB2MCL/CMC2MCL: Initial data: PWR[15..0] = 1,088, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWR[15..0] = 2,048, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning. The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048
28H: DLY[7..0]	[The delay time for the trigger] Initial data: DLY[7..0] = 0, data range: 0 to 255 Sets the delay time that is from the trigger signal input to start exposure. Delay time = 2 x DLY[7..0] (usecond)

Command No.	Command descriptions
29H: MOD6[7..0]	[The camera function mode] Initial data: 2 Sets the camera function mode. D[7..0] D7 to D4: Horizontal AOI 128 pixel shifts to the left when increase one for this. (Default: 0000) D3 to D0: Output taps and frame rate (STC-CMB2MCL/CMC2MCL/STC-CMB4MCL/CMC4MCL) 0 : 2TAP (85.0MHz, Horizontal: 2,048 pixels), 1 : 4TAP (85.0MHz, Horizontal: 2,048 pixels), 2 : 8TAP (85.0MHz, Horizontal: 2,048 pixels), 3 : 10TAP (80.0MHz, Horizontal: 2,048 pixels), 4 : Reserved 5 : 2TAP (85.0MHz, Horizontal: 1,024 pixels), 6 : 2TAP (85.0MHz, Horizontal: 512 pixels), 7 : 4TAP (85.0MHz, Horizontal: 1,024 pixels) 8 : 2TAP (85.0MHz, 2×2 Binning), 9 : 4TAP (85.0MHz, 2×2 Binning), 10 : 2TAP (85.0MHz, 4×4 Binning), 11 : 1TAP (85.0MHz, 8×8 Binning), 12 : 2TAP (85.0MHz, 2×2 Subsampling), 13 : 4TAP (85.0MHz, 2×2 Subsampling), 14 : 2TAP (85.0MHz, 4×4 Subsampling), 15 : 1TAP (85.0MHz, 8×8 Subsampling),
30H: ADC_gain [7..0]	[ADC gain] Initial data: AGC_gain[7..0] = 44, data range:0 to 63 Sets the ADC gain (programable gain). The proper value is different each pixel clock. The value for each pixel clock is stored the below address. The proper value loads to ADC_gain when the power on the camera or change the clock speed. E6H: ADC_gain for 10.625 MHz pixel clock E7H: ADC_gain for 21.250 MHz pixel clock E8H: ADC_gain for 42.500 MHz pixel clock E9H: ADC_gain for 48.000 MHz pixel clock EDH: ADC_gain for 5.3125 MHz pixel clock EFH: ADC_gain for 24.000 MHz pixel clock
31H: DGB[7..0]	[The digital gain] Initial data: DGB[7..0] = The factory adjusted value (The value of the address 0EH) Video level = (Input video level - CLAMP[7..0]) x (1 + DGB[7..0] / 128) + CLAMP[7..0] CLAMP[7..0]: The clamp level (The value of the address 38H)
38H:	[The clamp level] Initial data: CLAMP[7..0] = 40, data range: 0 to 255

Command No.	Command descriptions
40H: PSRD[7..0] 41H: PSRD[15..8]	[The start line of the variable partial scanning D] STC-CMB2MCL/CMC2MCL: Initial data: PSRD[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSRD[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning D. Actual start line of the partial scanning D = this value + 1
42H: PWRD[7..0] 43H: PWRD[15..8]	[The effective lines of the variable partial scanning D] STC-CMB2MCL/CMC2MCL: Initial data: PWRD[15..0] = 0, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWRD[15..0] = 0, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning D. The camera works with full scanning when PWRD[] is greater than below value: The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048
47H: HDR[7..0]	[HDR slope] Initial data: HDR[7..0] = 0 Sets the slope mode (HDR mode) and number of the slope. D[7..0] D7 to D6: HDR mode — 00: Normal, / 01: Pattern 1, / 10: Pattern 2, / 11: Free setting D5 to D2: No function — Sets always as "0000" D1 to D0: Number of the slope — 0
4BH: HDR[7..0]	[PGA] Initial data: PGA[7..0] = 0 Sets the gain for adjust the brightness. D[7..0] D7: No function — Sets always as "0" D6 to D5: PGA gain — 00: 1.0x gain, / 01: 1.2x gain, / 10: 1.4x gain, / 11: 1.6x gain D4 to D0: No function — Sets always as "00000"

Command No.	Command descriptions
4CH: H_START [7..0] 4DH: H_START [15..8]	[The horizontal effective pixels of changeable DVAL] When Camera Link horizontal pixels = 1,024: data range: 0 to 1,023 When Camera Link horizontal pixels = 512: data range: 0 to 511 When Camera Link horizontal pixels = 256: data range: 0 to 255 When Camera Link horizontal pixels = 204: data range: 0 to 203 Sets the horizontal start position of the changeable DVAL. When one increase or one decrease, the number of pixel is changed as the number of TAPs. For example, one increase at 8TAP, increases 8 pixels. One decrease at 4TAP, decreases 4 pixels. When the horizontal start position + the horizontal effective pixels is greater than number of horizontal The horizontal start position sets as 0 automatically. The horizontal effective pixels sets as number of horizontal pixels automatically. When the horizontal effective pixels is 0, The horizontal start position sets as 0 automatically. The horizontal effective pixels sets as number of horizontal pixels automatically. 4DH.D7 LVAL selection 0: Continuous (Horizontal equals DVAL), 1: Continuous (Full horizontal)
4EH: H_NUMBER [7..0] 4FH: H_NUMBER [15..8]	[The horizontal effective pixels of changeable DVAL] When Camera Link horizontal pixels = 1,024: data range: 0 to 1,024 When Camera Link horizontal pixels = 512: data range: 0 to 512 When Camera Link horizontal pixels = 256: data range: 0 to 256 When Camera Link horizontal pixels = 204: data range: 0 to 204 Sets the horizontal effective number of pixels of the changeable DVAL. When one increase or one decrease, the number of pixel is changed as the number of TAPs. For example, one increase at 8TAP, increases 8 pixels. One decrease at 4TAP, decreases 4 pixels. (For example) Settings: Number of TAP is 8TAPS, Horizontal start position is 10, Horizontal effective pixels is 20 Result: Pixel of the left end of the image: 81th pixel The horizontal size: 160 pixels When the horizontal start position + the horizontal effective pixels is greater than number of horizontal The horizontal start position sets as 0 automatically. The horizontal effective pixels sets as number of horizontal pixels automatically. When the horizontal effective pixels is 0, The horizontal start position sets as 0 automatically. The horizontal effective pixels sets as number of horizontal pixels automatically.

Command No.	Command descriptions
50H: PSRE[7..0] 51H: PSRE[15..8]	[The start line of the variable partial scanning E] STC-CMB2MCL/CMC2MCL: Initial data: PSRE[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSRE[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning E. Actual start line of the partial scanning E = this value + 1
52H: PWRE[7..0] 53H: PWRE[15..8]	[The effective lines of the variable partial scanning E] STC-CMB2MCL/CMC2MCL: Initial data: PWRE[15..0] = 0, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWRE[15..0] = 0, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning E. The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048
56H: KN1P[7..0]	[Knee1 prameter] Initial data: KN1P[7..0] = ??, data range: 0 to ??? Sets the exposure time for the electronic shutter for the knee1 point, as % for the exposure time. Knee1 exposure = EXPTA[23..0] x 129 x (1 CLK cycle) x KN1P[7..0] / 100
57H: KN2P[7..0]	[Knee2 prameter] Initial data: KN2P[7..0] = ??, data range: 0 to ??? Sets the exposure time for the electronic shutter for the knee2 point, as % for the exposure time. Knee1 exposure = EXPTA[23..0] x 129 x (1 CLK cycle) x KN2P[7..0] / 100
5BH: Vlow2[7..0]	[Vlow2 voltage] Initial data: 96, data range: 0 to 255 Sets the Vlow2 voltage for the HDR saturation level voltage.
5FH: Vlow3[7..0]	[Vlow3 voltage] Initial data: 96, data range: 0 to 255 Sets the Vlow3 voltage for the HDR saturation level voltage.
68H: [7..0]	[Reverse mode] Initial data: 0 Sets the image output direction. D[7..0] D7 to D6: No function Sets always as "00" D5: Vertical mirror image 0: Normal image, 1: Vertical mirror image D4: Horizontal mirror image 0: Normal image, 1: Horizontal mirror image D3 to D0: No function Sets always as "0000"

Command No.	Command descriptions
78H: TESTP[7..0]	[Test pattern selection] Initial data: TESTP[7..0] = 00H Sets the test pattern output form the camera. D[7..0] 00H: Video output, 01H: Gray schale, 02H: Ramp wave, 03H: 100% White, 04H: White clip, 05H: Color bar (RGB bayer), Others: Black
79H: EFFCT[7..0]	[Image effect selection] Initial data: EFFCT[7..0] = 00H Sets the imgae effect. D[7..0] D7: Negative video / Positive video selection 0: Positive image, 1: Negative image D6 to D0: Image effect 00H: No effect (Original), 01H: 9bit gradation, 02H: 8bit gradation, 03H: 7bit gradation, 04H: 6bit gradation, 05H: 5bit gradation, 06H: 4bit gradation, 07H: 3bit gradation, 08H: 2bit gradation, 09H: 1bit gradation, 0A to 7FH: No function (Prohibited setting. Do not set these values)

Command No.	Command descriptions
90H: PSRF[7..0] 91H: PSRF[15..8]	[The start line of the variable partial scanning F] STC-CMB2MCL/CMC2MCL: Initial data: PSRF[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSRF[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning F. Actual start line of the partial scanning F = this value + 1
92H: PWRF[7..0] 93H: PWRF[15..8]	[The effective lines of the variable partial scanning F] STC-CMB2MCL/CMC2MCL: Initial data: PWRF[15..0] = 0, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWRF[15..0] = 0, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning F. The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048
94H: PSRG[7..0] 95H: PSRG[15..8]	[The start line of the variable partial scanning G] STC-CMB2MCL/CMC2MCL: Initial data: PSRG[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSRG[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning G. Actual start line of the partial scanning G = this value + 1
96H: PWRG[7..0] 97H: PWRG[15..8]	[The effective lines of the variable partial scanning G] STC-CMB2MCL/CMC2MCL: Initial data: PWRG[15..0] = 0, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWRG[15..0] = 0, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning G. The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048

Command No.	Command descriptions
98H: PSRH[7..0] 99H: PSRH[15..8]	[The start line of the variable partial scanning H] STC-CMB2MCL/CMC2MCL: Initial data: PSRH[15..0] = 0, data range: 0 to 1,087 STC-CMB4MCL/CMC4MCL: Initial data: PSRH[15..0] = 0, data range: 0 to 2,047 Sets the start line of the variable partial scanning H. Actual start line of the partial scanning H = this value + 1
9AH: PWRH[7..0] 9BH: PWRH[15..8]	[The effective lines of the variable partial scanning H] STC-CMB2MCL/CMC2MCL: Initial data: PWRH[15..0] = 0, data range: 0 to 1,088 STC-CMB4MCL/CMC4MCL: Initial data: PWRH[15..0] = 0, data range: 0 to 2,048 Sets the number of the effective lines (image height) of the variable partial scanning H. The camera works with full scanning when the total effective lines of the eight partial (PWR[] + PWRB[] + PWRC[] + PWRD[] + PWRE[] + PWRF[] + PWRG[] + PWRH[]) is grater than below value: STC-CMB2MCL/CMC2MCL: 1,088 STC-CMB4MCL/CMC4MCL: 2,048
D9H: [7..0]	[LED control] Initial data: [7..0] = 0 Sets the LED control method. D[7..0] D7 to D1: No function — Sets always as "0000000" D0: LED control — 0: LED control by the function mode, When continuous mode: LED On When trigger mode: LED On 1 second then Off 1 second (repeatedly) 1: LED Off

3. The Sequence for Saving Commands to the EEPROM

Please use the sequence below in order to save commands to the EEPROM.

1) Set “1” to the 80H.0 for the accept “write control to the EEPROM”.

2) Send the command and the save data with the EEPROM access command, which is set “1” for the page selection.

3) The camera send back the one of the below receiving code after write EEPROM.
   01H: OK
   10H: EEPROM write error

4) 80H.0 is changed to “0” automatically after write EEPROM.

Note.1) DO NOT save to the EEPROM when 80H.0 is “0”.

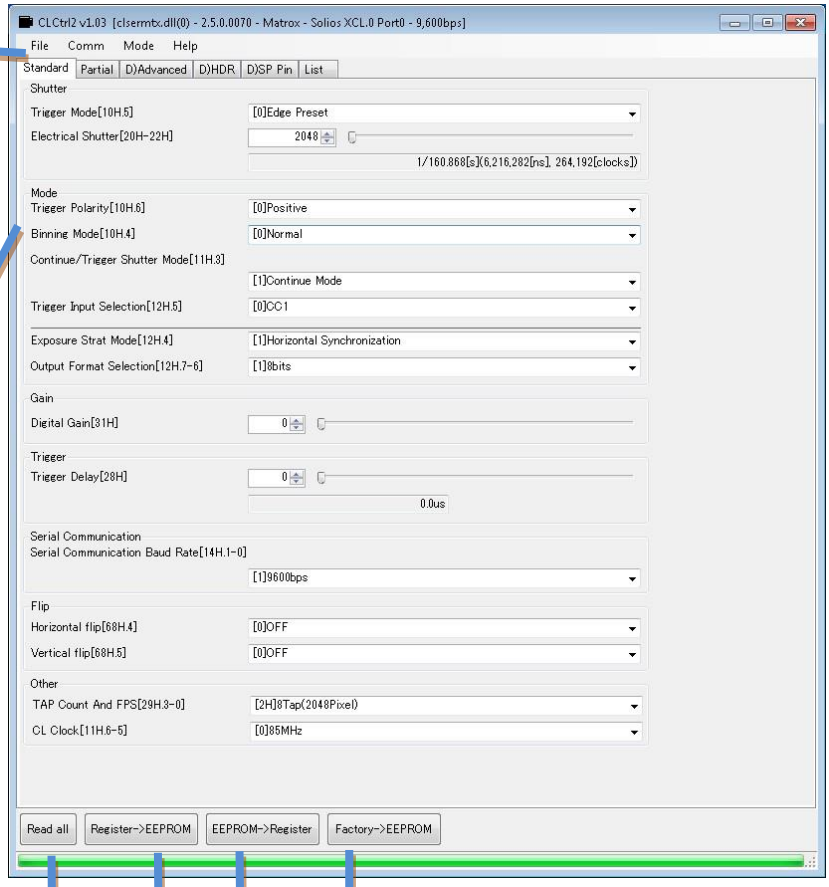
Note.2) When save the multiple sequence command to the EEPROM, all data save to the EEPROM by one operation from 1) to 4).
Example of the multiple sequence command: “10H, 11H, 12H and 13H” or “22H, 23H and 24H”.

Note.3) When save the multiple command data, which is not sequence command, to the EEPROM, it is necessary to operate the number of times from 1) to 4).
Example of the multiple command: “10H, 13H, 19H and 1BH” or “20H, 23H and 25H”.

VII. Control Software

A. Summary

After installing the control software, and launching “CLCtrl2.exe”, the main window appears as below.



Menu
As for the detail, please refer to the next page.

Camera Setting Parameters
As for the detail, please refer to the next chapter Error! Reference source not found..

Load the factory saved settings data to EERPOM .
As for the detail, please refer to the next chapter Error! Reference source not found..

Load the previously saved settings data from EERPOM to Register.
As for the detail, please refer to the next chapter Error! Reference source not found..

Save the camera setting data on the register to EEPROM.
As for the detail, please refer to the next chapter Error! Reference source not found..

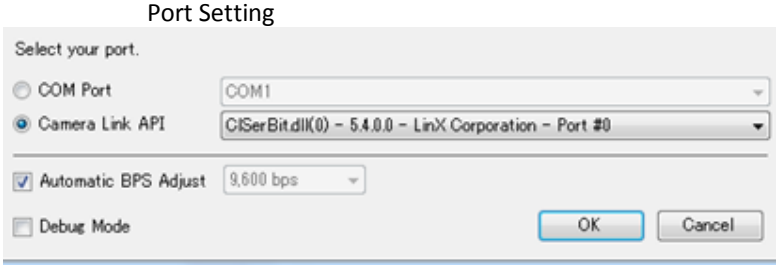
Read the camera setting data from Register.
As for the detail, please refer to the next chapter Error! Reference source not found.

1. File

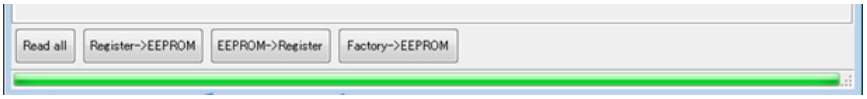
Open [From File to Register]
Open the camera setting file (.i2c).
Save as[From Register to File]
Save the current camera setting data on the register to the PC as i2c file.
Open[From File to EEPROM]
Open the camera setting file (.i2c) that is read at power on.
Save as[From EEPROM to File]
Save the camera setting data on EEPROM to the PC as i2c file.
Quit
Exit the control software.

2. Comm

Port Setting



[Select your port]
COM port When the Graber Board supports COM port, Please select this command.
Camera Link API When the Graber Board supports Camera Link API, Please select this command.
[Automatic BPS Adjust]
Select the serial comunication speed automatically. When un-checked the box, comunication speed can be selected.
[Debug Mode]
Basically un-checked the box, when checked the box, transfer data can be monitored through 3rd party software.



Read All
Read the setting of all data from the camera register. This setting data on the register cannot be saved without saving the EEPROM (Register → EEPROM)
Register → EEPROM
Save the register data into the EEPROM on the camera. When the camera turns off, the data remains on the EEPROM.
EEPROM → Register
Read the EEPROM data into the register. When saved data needs to be used again, this can be done.
Factory → EEPROM
Restore the Factory setting data from EEPROM to the register.

3. Mode

Language

Select the language from English or Japanese.

4. Help

Advanced Operation

When password (sentechcamera) is input, additional functions appear for power user. SP Pin tab can be used.

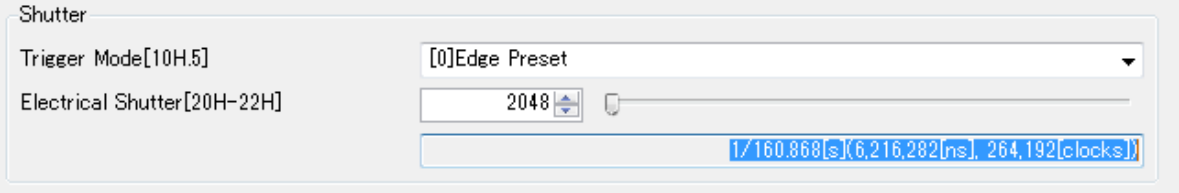
Version Information

Software information window appear.

B. Software Function (Standard)

This tab has basic camera function, the number (like [10H.5]) beside of the function is register address. When direct register access is needed, please refer to Section VI: The Communication Protocol Specifications.

1. Shutter



Trigger Mode

Edge Preset — The camera exposure starts at the rising (or falling) edge of the trigger pulse. Exposure duration time is preset.

Pulse Width — The camera exposure starts at the rising (or falling) edge of the trigger pulse and stops at the falling (or rising) edge of the trigger pulse.

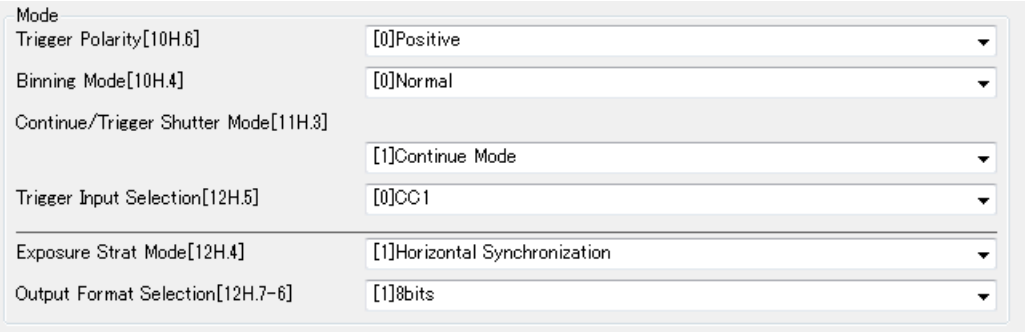
As for the detail of Trigger Mode, please refer to Section V: Camera Function Modes.

Electrical Shutter

Electrical shutter setting can be set through the slide bar or set through the actual register value. Actual exposure time appears on the bottom of the slide bar.

As for the detail of the exposure time setting, please refer to Section VI: The Communication Protocol Specifications.

2. Mode



Positive	Positive Signal is available as Trigger
Negative	Negative Signal is available as Trigger

Binning Mode

Normal	Disable the binning.
Binning	Enable the binning.

Continue / Trigger Shutter Mode

Continue Mode	Obtaining the image from the camera automatically. The trigger is generated inside of the camera continuously.
Trigger Shutter Mode	Obtaining the image from the external trigger timing. When this mode is selected Edge Preset, Pulse Width on the [Trigger Mode] are available.

As for the details of the Continue Mode, Trigger Shutter Mode, please refer to Section V: Camera Function Modes

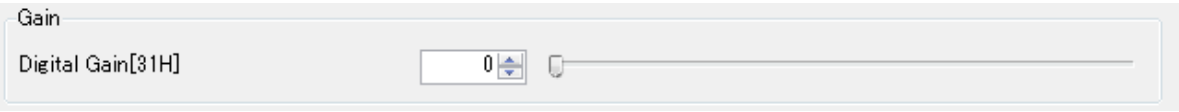
Trigger Input Selection

CC1:	Trigger Signal input from camera link connector on pin CC1.
SP4:	Trigger Signal input from I/O port. As for the details on this port, please refer to Section VII-A: Using the Trigger Signal through 6pin.

Exposure Start Mode

Normal	Exposure is going to start after trigger input. The exposure can start during the video out from the camera with horizontal noises.
Horizontal Synchronization	The exposure can start during the video out from the camera without horizontal noises. The maximum delay to start exposure from the trigger input in 1H.
Output Format Selection	Video output bit can be selected 8/10/12 bit. Video output bit is different for each mode. As for the relation of each more to video output, please refer to Section IV-B: The Vertical Timings.

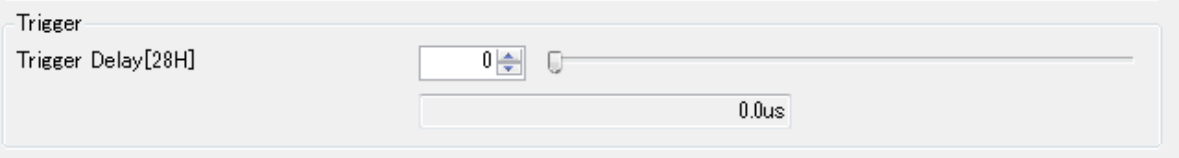
3. Gain



Digital Gain

The value of digital gain. As for the details of gain calculation, please refer to the command: 31H.

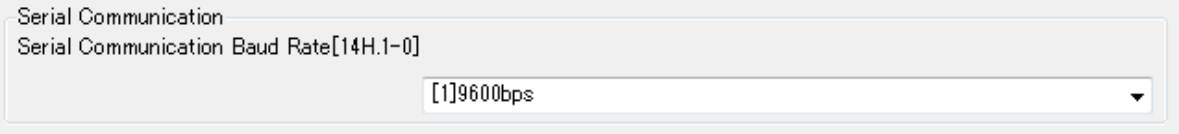
Trigger



Trigger Delay

The delay time for the trigger. As for the details of delay time calculation, please refer to the command: 28H.

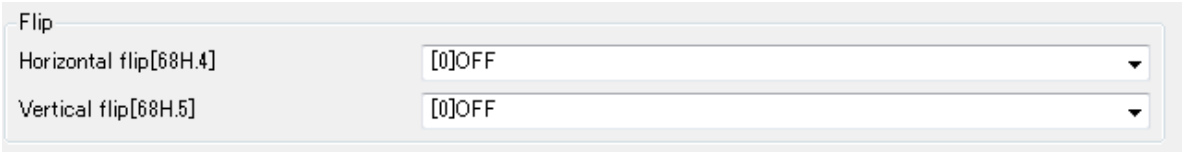
4. Serial Communication



Serial Communication Baud Rate

Baud rate can be selected.

5. Flip



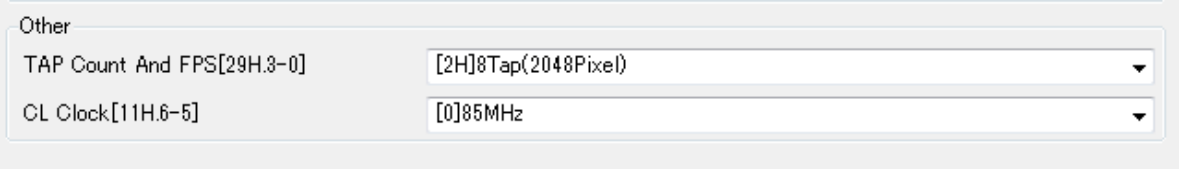
Horizontal Flip

OFF Normal Image
ON Horizontal Mirror Image

Vertical Flip

OFF Normal Image
ON Vertical Mirror Image

6. Other



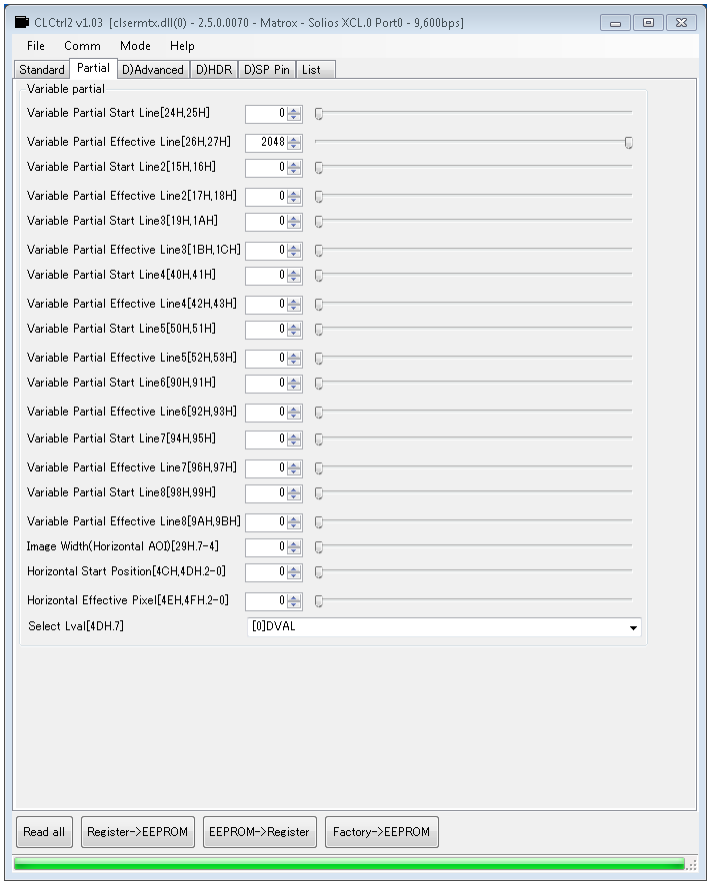
TAP Count and FPS

TAP number can be selected by frame rate, Camera Link Output Bit, Video mode. As for the details, please refer to Section IV-B: Vertical Timing

CL Clock

Camera Link Output Pixel Clock Frequency (MHz) supports High Speed mode and Low Speed mode. Clock speed can be selected by the frame rate, Camera Link Output, and video mode. As for the details, please refer to section IV-B: Vertical Timing.

C. Software Function (Partial)



Variable Partial

Partial Scan can be set. As for the details, please refer to Section VII: The Communication Protocol Specifications

D. Software Function (Advanced)

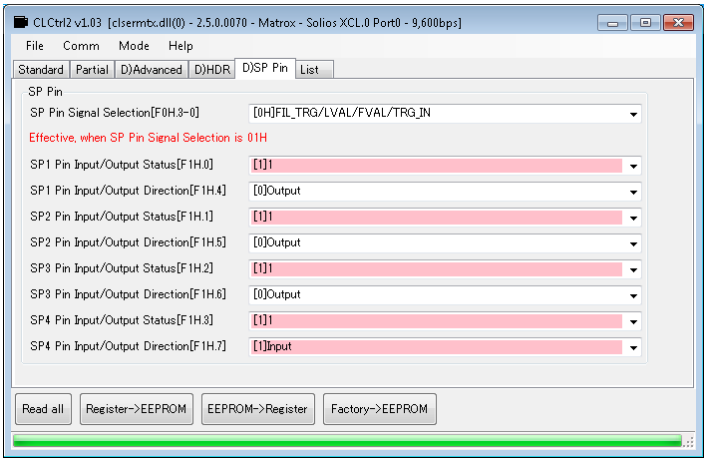
This tab is used for factory setting, please DO NOT use this tab.

E. Software Function (HDR)

This tab is used for the power user to control the Gamma deeply. Do not use the color model.

F. Software Function (SP Pin)

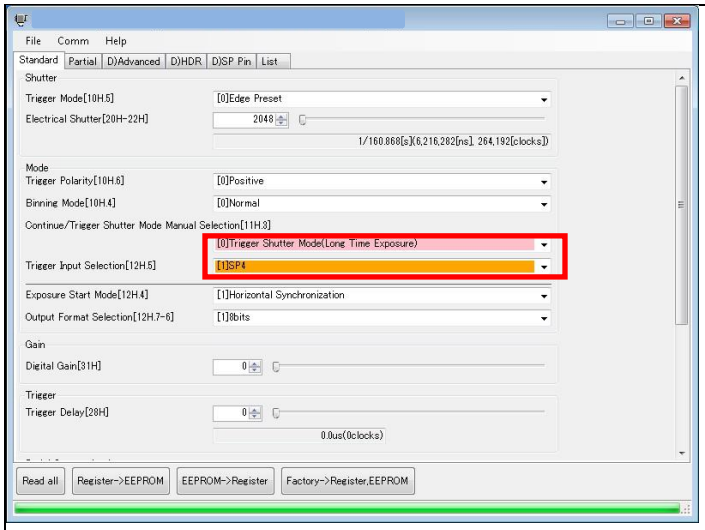
External trigger can be used. As for the detail, please refer to Section VIII-A: Using the Trigger Signal through 6pin

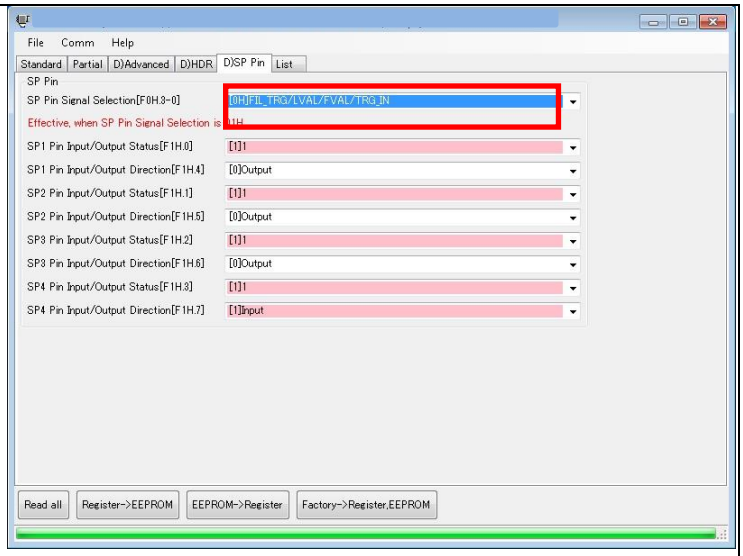


VIII. Actual Camera Setting & Technical Notes

A. Using the Trigger Signal Through 6pin

1) Select the “[0]Trigger Shutter Mode (Long Exposure)” on Continue / Trigger Shutter Mode Selection at the Standard tab through the control software. (CLCtrl2)
2) Select the “[0H] FIL_TRG/LVAL/FVAL_TRG_IN” on SP_Pin Signal Selection at SP_Pin Tab.
3) Input the trigger signal through Pin2. As for using the software, please refer to Section VI: Control Software





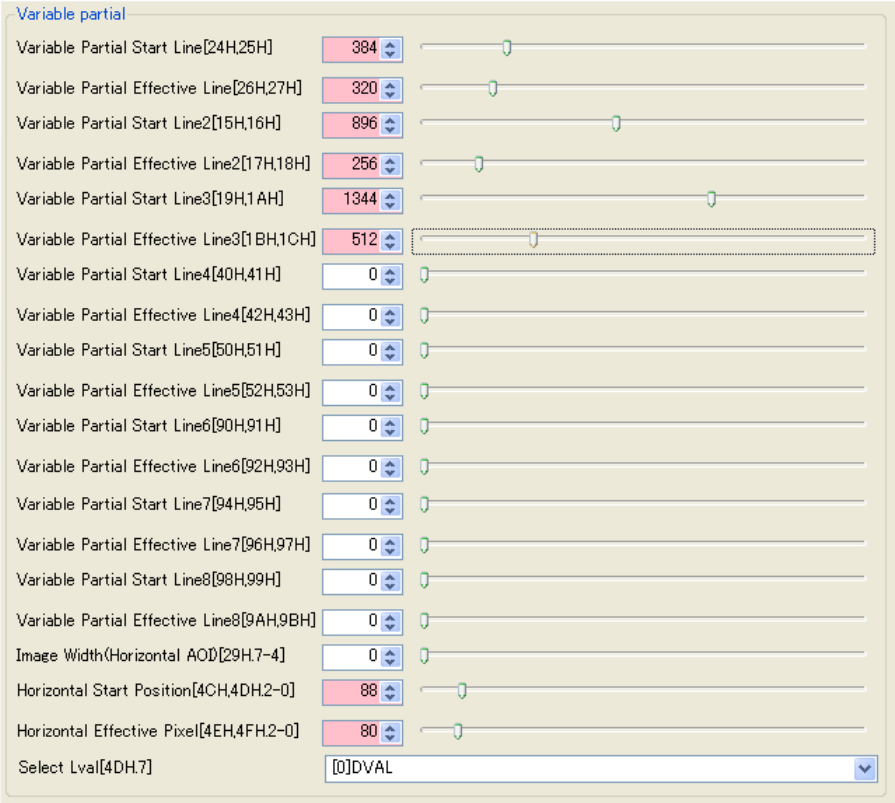
SP_Pin Signal Selection Table

Pin No	5	4	3	2
Addr=F0	SP1	SP2	SP3	SP4
0	AfterTrigger FILTER	LVAL	FVAL	**Trigger Input**
1	F1h.0	F1h.1	F1h.2	F1h.3
2	CC1	T_EXP1	FRAME_REQ	HIGH in Exposure
3	CC1	T_EXP1	FRAME_REQ	FVAL
4 ～15	Reserved			

B. Example Setting of AOI

Ex:

Setting on 8 TAP (2048 Pixel)



e.g.

The value of the horizontal effective pixel. The horizontal effective pixels of changeable DVAL for each setting.

TAP Number = 8
H_STRAT; Horizontal Start Position = 704
H_NUMBER; Horizontal Available Pixel Number = 640

The Horizontal Effective Pixel

Register[4CH,4DH2-0] ＝704 ÷ TAP Number ÷ Binning or Subsampling number = 704 ÷ 8 ÷ 1 = 88

The Horizontal Effective Pixels of Changeable DVAL

Register[4EH,4FH2-0] ＝640 ÷ TAP Number ÷ Binning or Subsampling number = 640 ÷ 8 ÷ 1 = 80

Mode(29H)	**The horizontal effective pixel**	**The horizontal effective pixels of changeable DVAL**	TAP Number	Binning or Subsampling number	Horizontal Pixel	
0	352	320	2	1	2048	Progressive
1	176	160	4	1	2048	Progressive
2	88	80	8	1	2048	Progressive
3 *	70	64	10	1	2040	Progressive
4	352	320	2	1	2048	Progressive
5	352	320	2	1	1024	Progressive
6	352	320	2	1	512	Progressive

7	176	160	4	1	1024	Progressive
8	176	160	2	2	2 x 2	Binning
9	88	80	4	2	2 x 2	Binning
10	88	80	2	4	4 x 4	Binning
11	88	80	1	8	8 x 8	Binning
12	176	160	2	2	2 x 2	Subsampling
13	88	80	4	2	2 x 2	Subsampling
14	88	80	2	4	4 x 4	Subsampling
15	88	80	1	8	8 x 8	Subsampling

Note: Horizontal pixel is 2040 on 10 TAP mode, then formula of “The Horizontal Effective Pixel” is different.

The Horizontal Effective Pixel

Register[4CH,4DH2-0] ＝(Horizontal start position -4) ÷ 10 TAP ÷ 1

Revisions

Revision	Date (D/M/Y)	Changes	Changes
0.02	July 11, 2011	Created Document	
0.03	Oct. 24, 2011	Update Changed the dimension drawing Added the LED information	
0.04	Feb 6, 2012	Update Changed the noise level, digital gain, power consumption, dimensions and weight Deleted the 12-bit output	
0.10	Feb 6, 2013	Update Electronic Specifications Spectral Sensitivity Medium / Full Configuration	
0.11	March 6, 2013	Update Added Input / Output signal Circuit	
2.0	May 7, 2013	New Document Updated to Full Version	
2.01	May 20, 2013	Update Revised IO Information	
2.02	May 20, 2013	Update Revised 12H [5]	
2.03	June 5, 2013	Update Revised Frame Rates on Specifications	
2.04	July 16, 2013	Update Added Pixel Defect Correction on D8H	

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